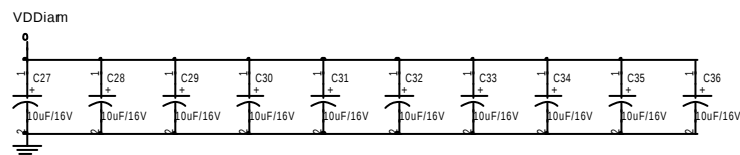
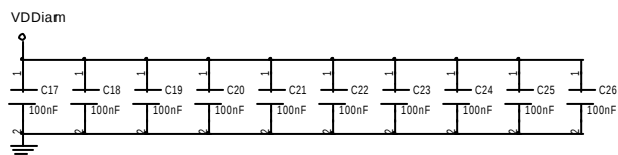
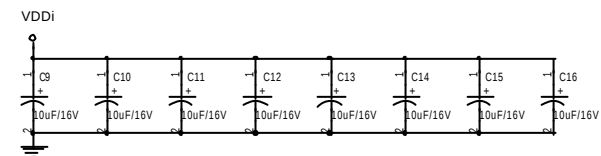
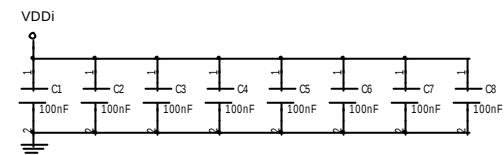
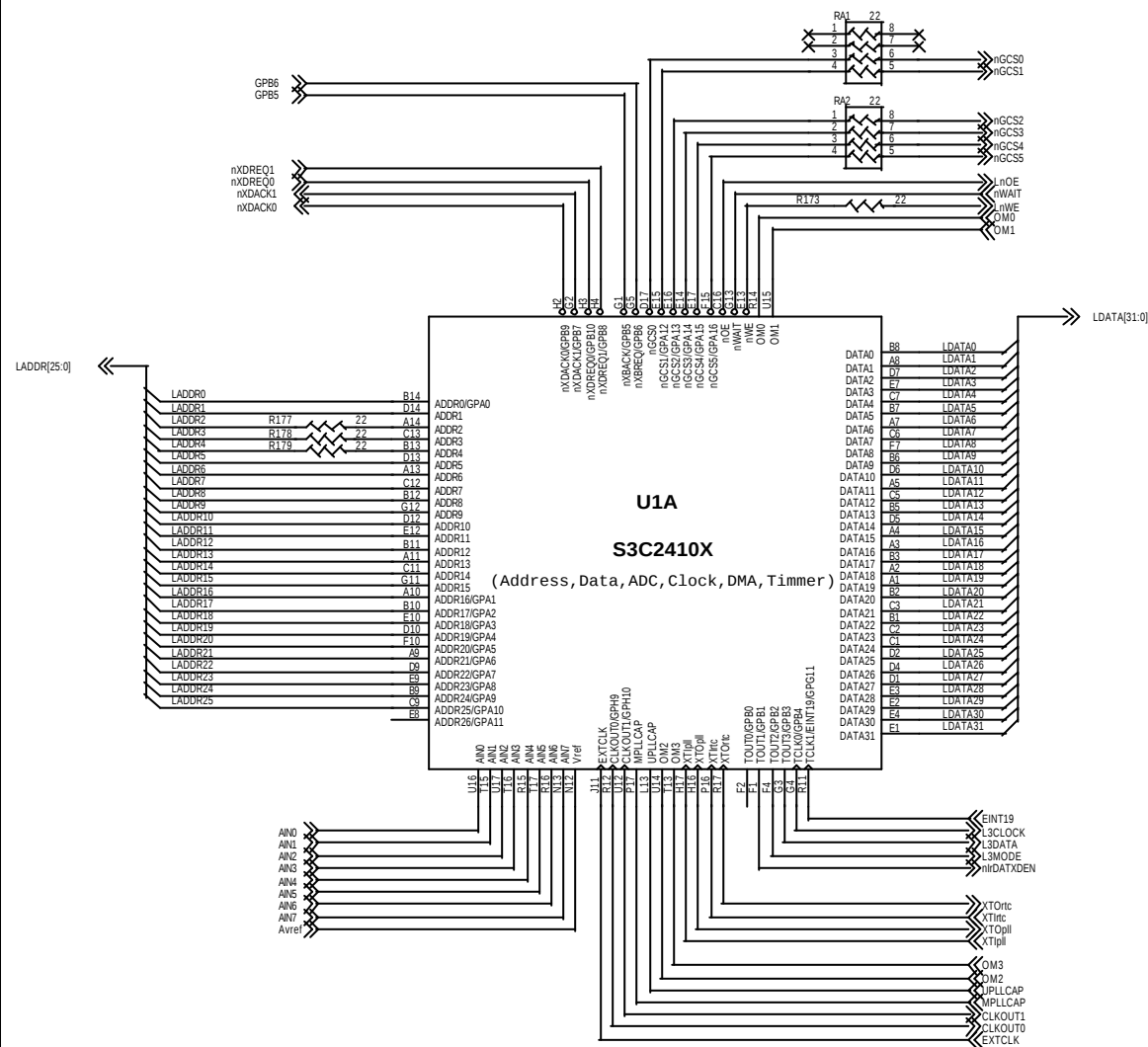
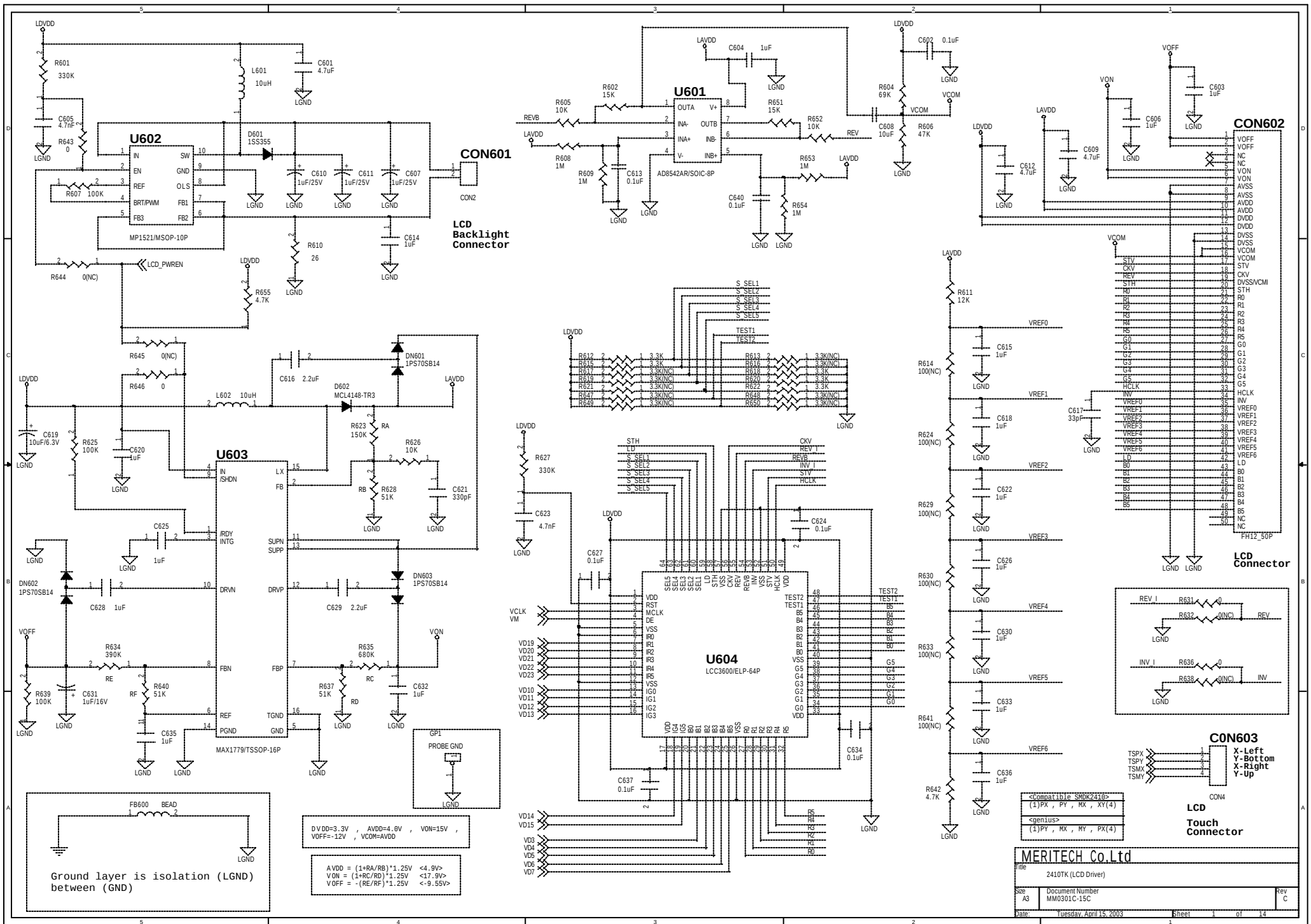


[illegible][illegible][illegible]

NOTE!!!
Lxxxx signals should be routed as short as possible.





<Compatible SMDK2410>

(1)PX, PY, MX, XY(4)

<genius>

(1)PY, MX, MY, PX(4)

MERITECH Co.Ltd

2410TK (LCD Driver)

Document Number MM0301C-15C

Date: Tuesday, April 15, 2003

Sheet 1 of 14

Rev C

POWER

(J15 , SW2 -)

CON401



S401



ON OFF

FP401
PICO-FUSE/3A

VDD5V

U401

(or EZ1117-3.3/T0263-3P)

AME1085DCDT-3/T0263-3P

VIN
ADJGND
VOUT

C402
10uF/16V

C403
10uF/10V

C404
100nF

TP401
TEST POINT

3.3V

J301

MVDD33V

**MEM
Check**

J302

VDDMCP

J303

VDD33V

**I/O
Check**

VCC

VDDIO

VDDADC

U402

(or EZ1117-3.3/T0263-3P)

AME1085DCDT-3/T0263-3P

VIN
ADJGND
VOUT

C407
10uF/16V

C408
10uF/10V

C409
100nF

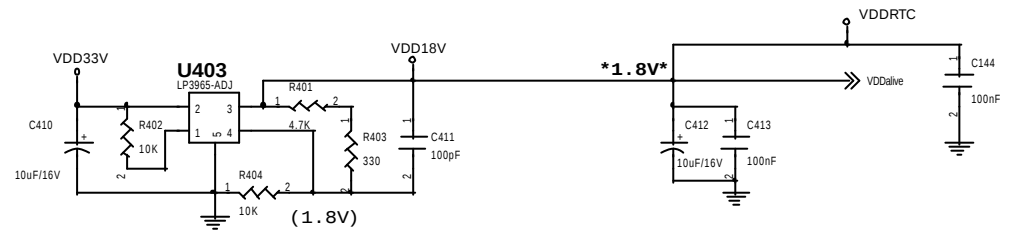
TP402
TEST POINT

3.3V

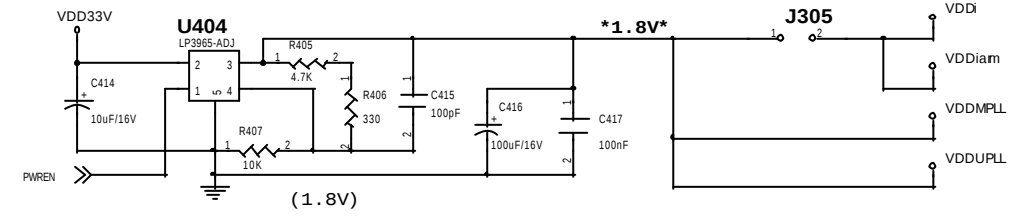
J304

LDVDD

**LCD
Check**



**Core
Check**



Power

MERITECH Co.Ltd

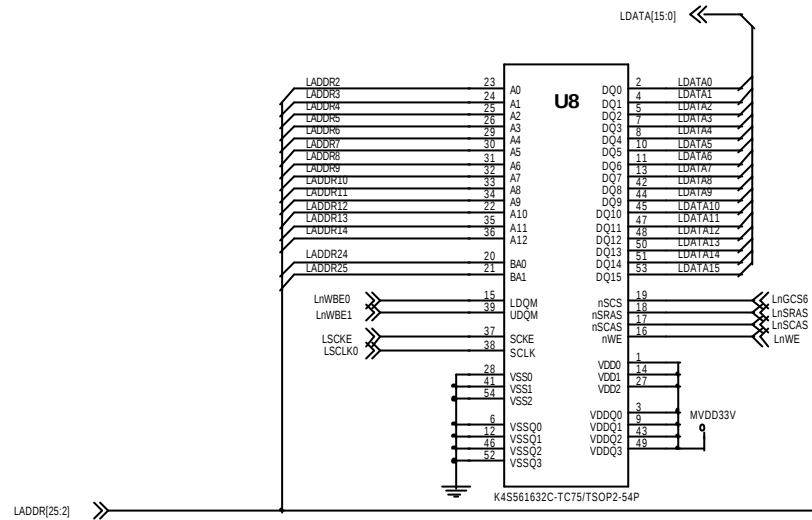
File 2410TK (POWER)

Size A3 Document Number MM0301C-15C

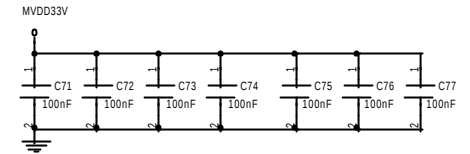
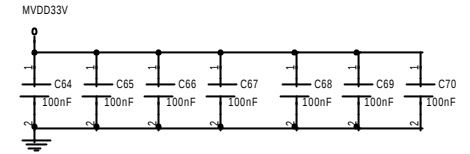
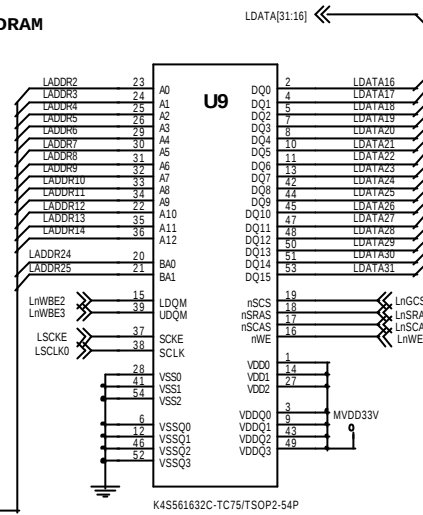
Rev C

Date: Tuesday, April 15, 2003

Sheet 1 of 14



SDRAM



LADDR[25:2] >>

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File 2410TK (SDRAM)

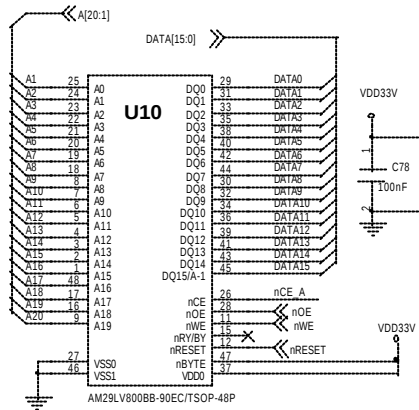
Size A3 Document Number MM0301C-15C

Date: Tuesday, April 15, 2003

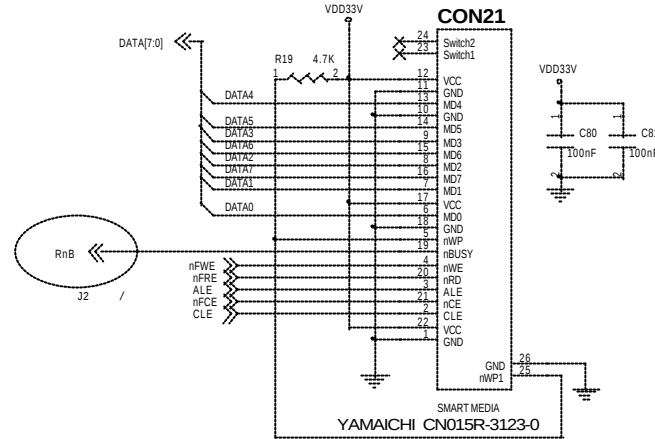
Sheet 4 of 12

Rev C

AMD Flash Memory

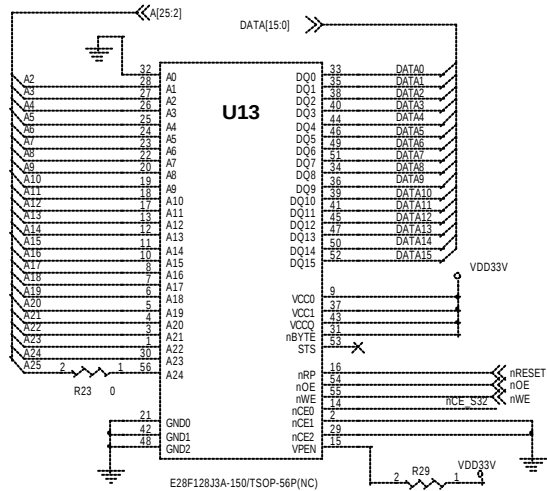
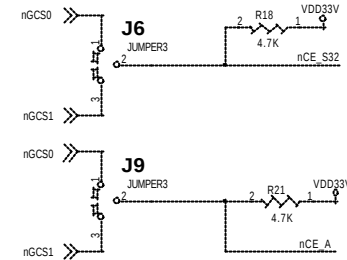


SmartMedia Card (NAND Flash memory)

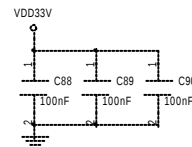
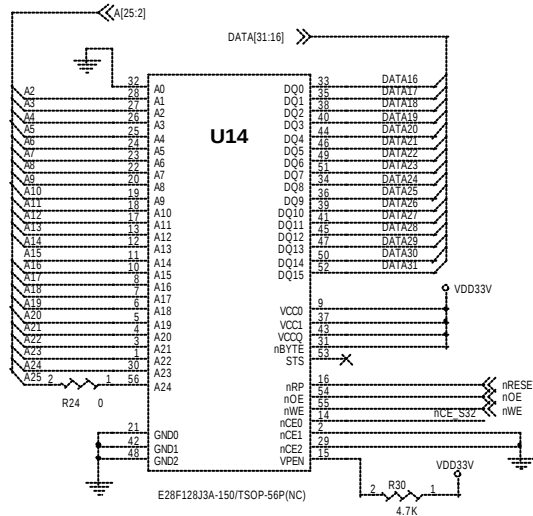
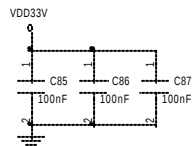


FLASH Memory Select

FLASH	J6	J9
AMD Flash	2-3	1-2
Strata 32bit	1-2	2-3

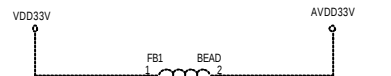


INTEL StrataFlash Memory(SMD Unload)

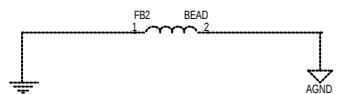


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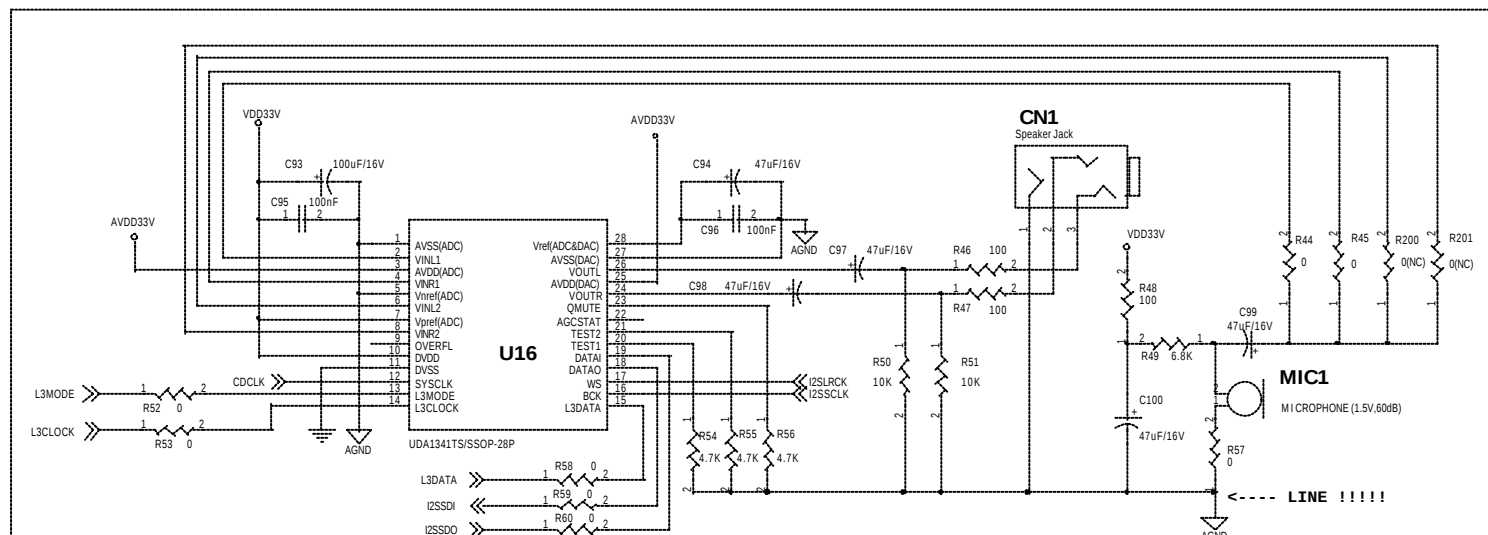
File	2410TK (FLASH)	Rev	C
Size	Document Number		
A3	MM0301C-15C		
Date:	Tuesday, April 15, 2003	Sheet	5 of 12



Power is isolation Analog (AVDD33V)
between Digital (VDD33V)



Ground layer is isolation Analog
(AGND) between Digital (GND)



IIS Interface (Audio Codec)

MERITECH Co.Ltd

File 2410TK (SOUND)

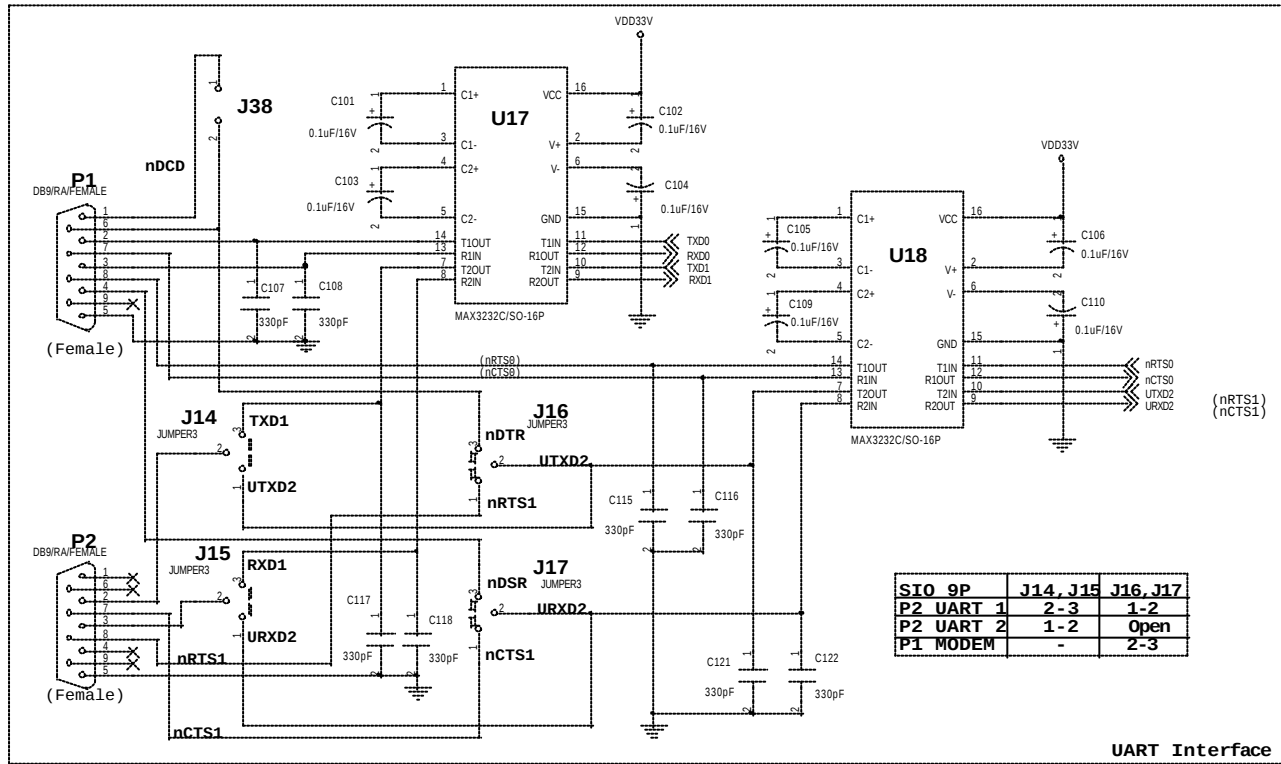
Size A3 Document Number MM0301C-15C

Rev C

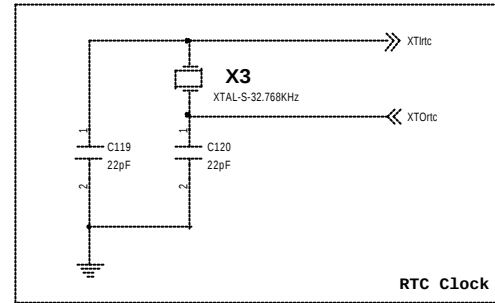
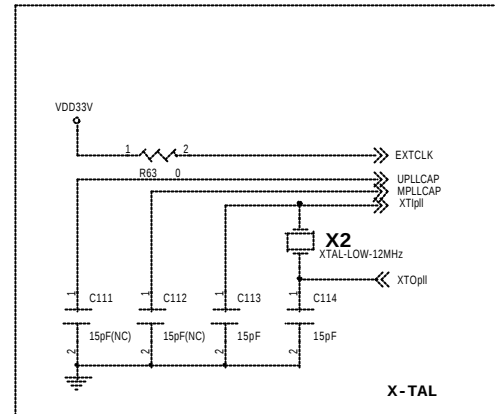
Date: Tuesday, April 15, 2003

Sheet 6 of 12

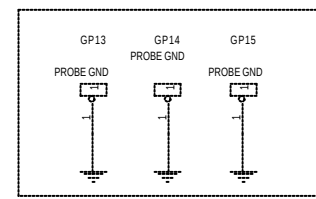
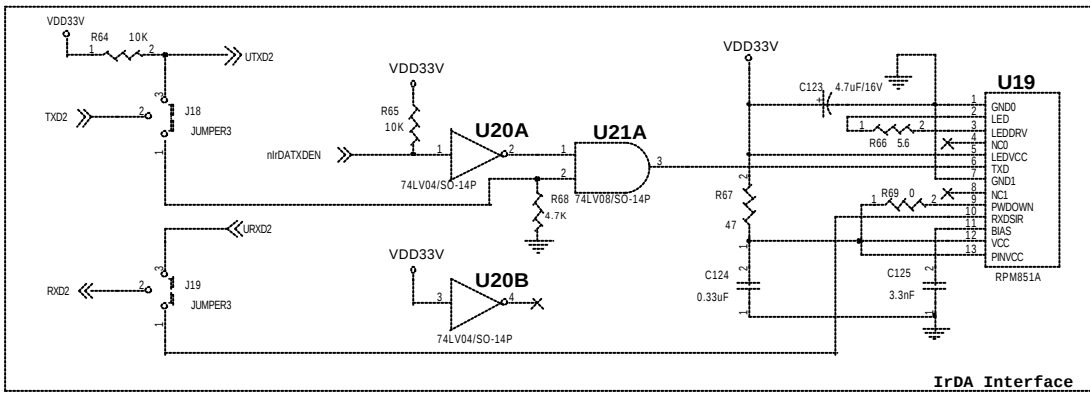
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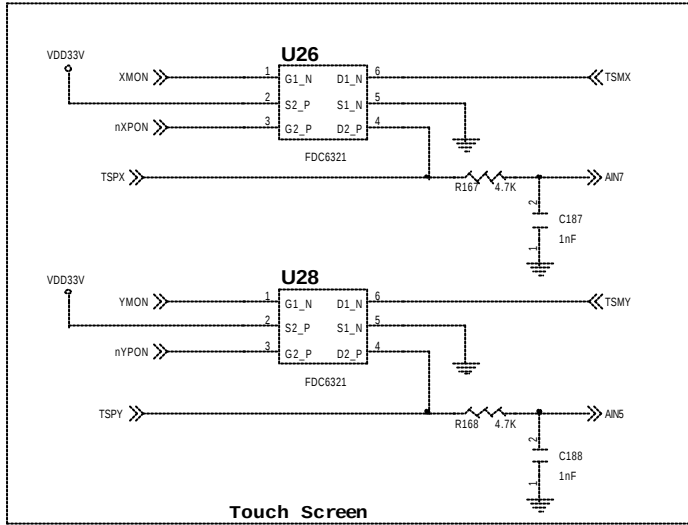


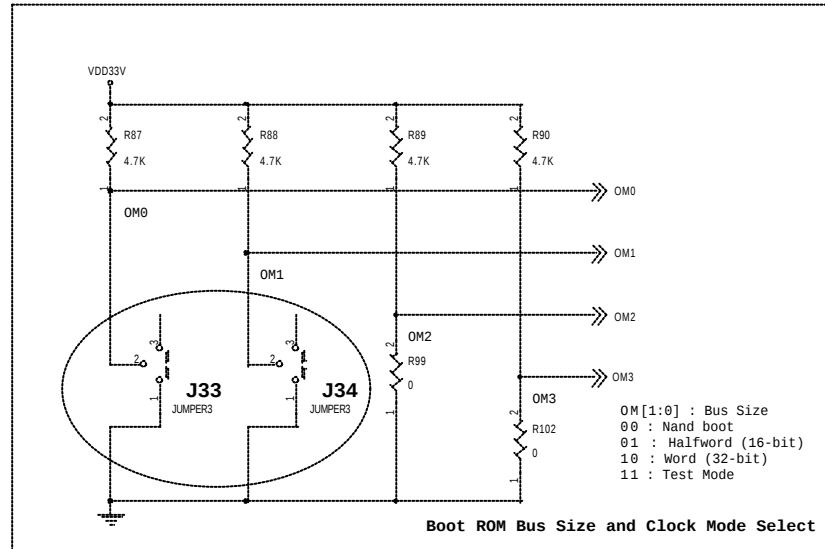
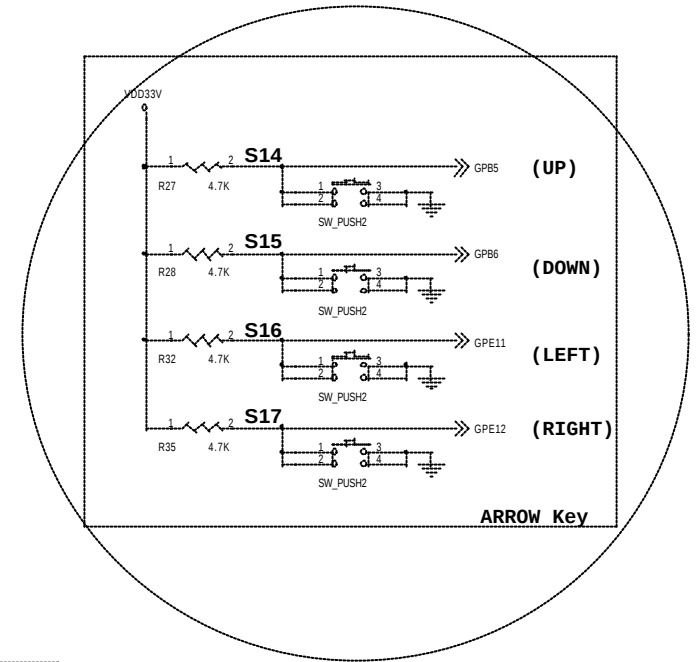
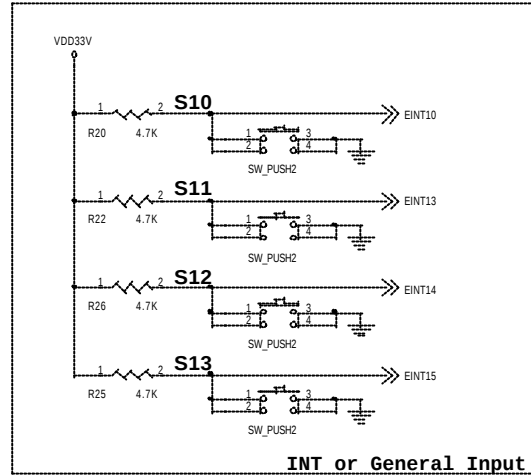
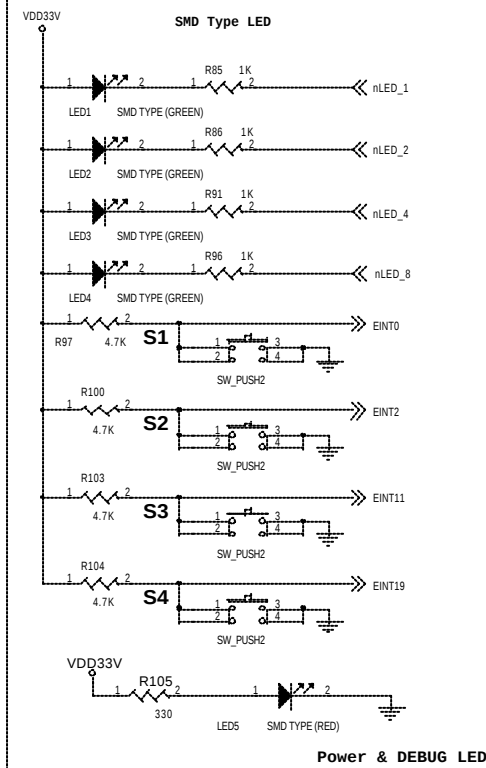
STO 9P	J14, J15	J16, J17
P2 UART 1	2-3	1-2
P2 UART 2	1-2	Open
P1 MODEM	-	2-3



	J18	J19
IrDA	1-2	1-2
UART2	2-3	2-3







S3C2410X01	J200
Open	No use ICE
Short	use ICE

