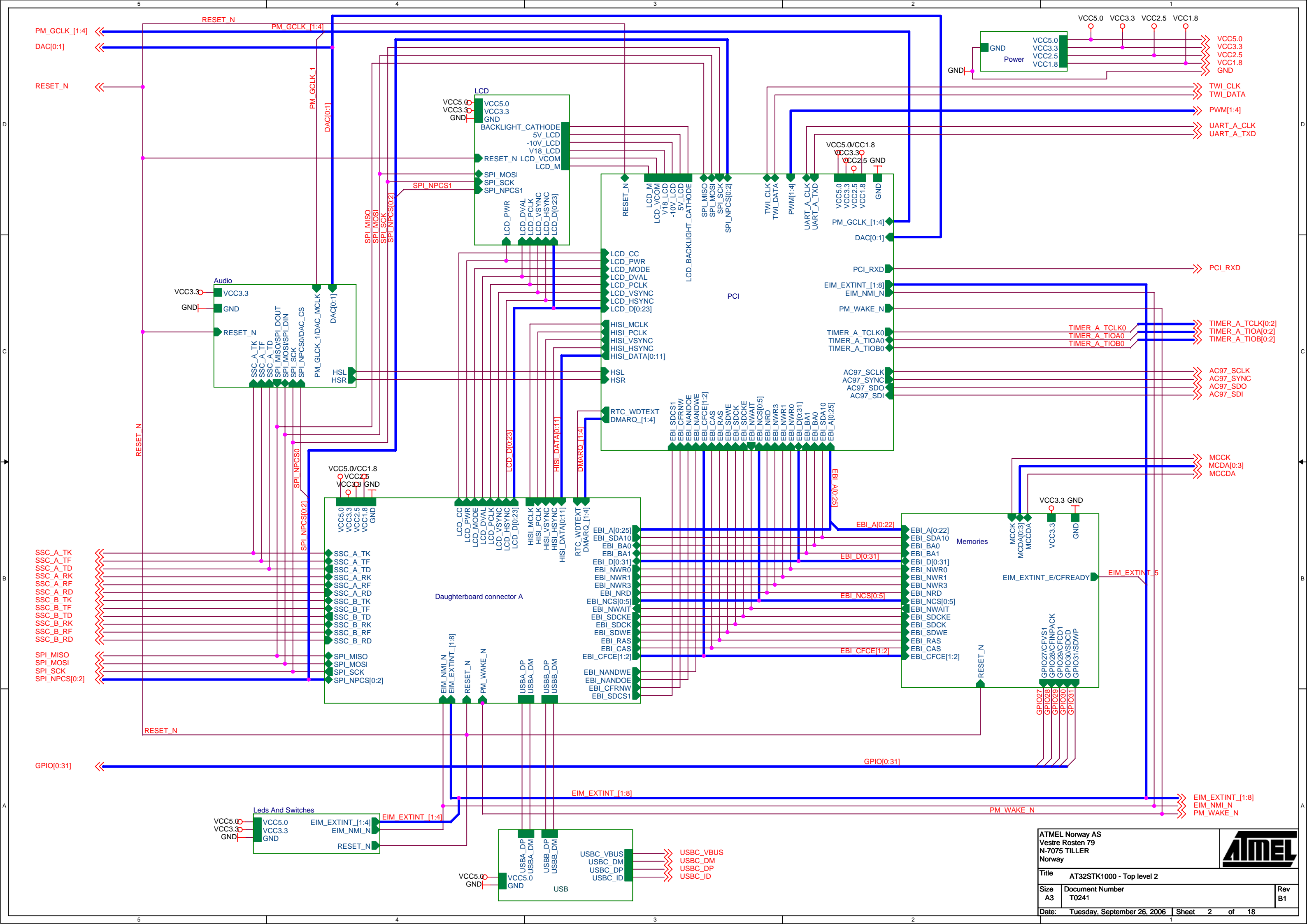
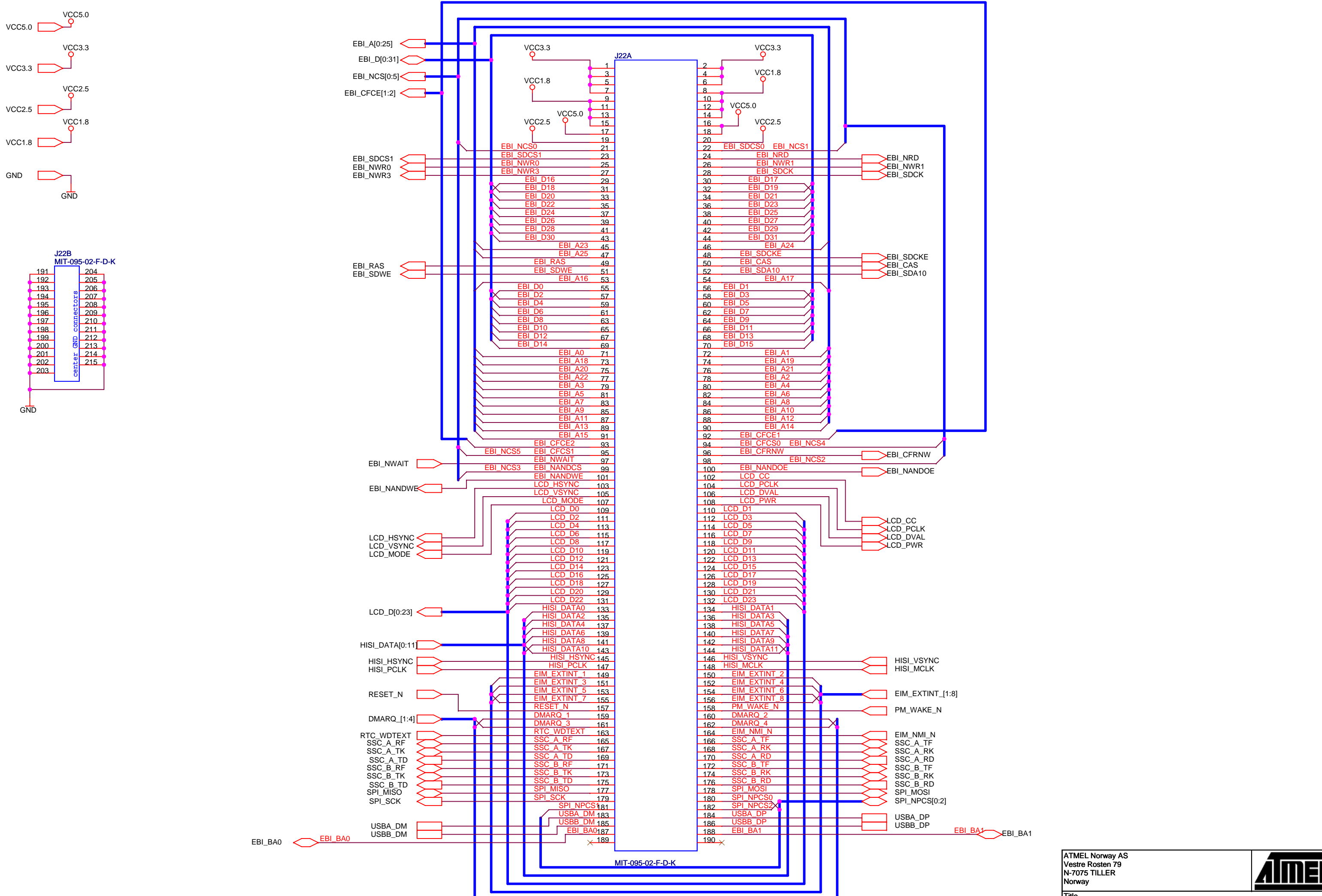
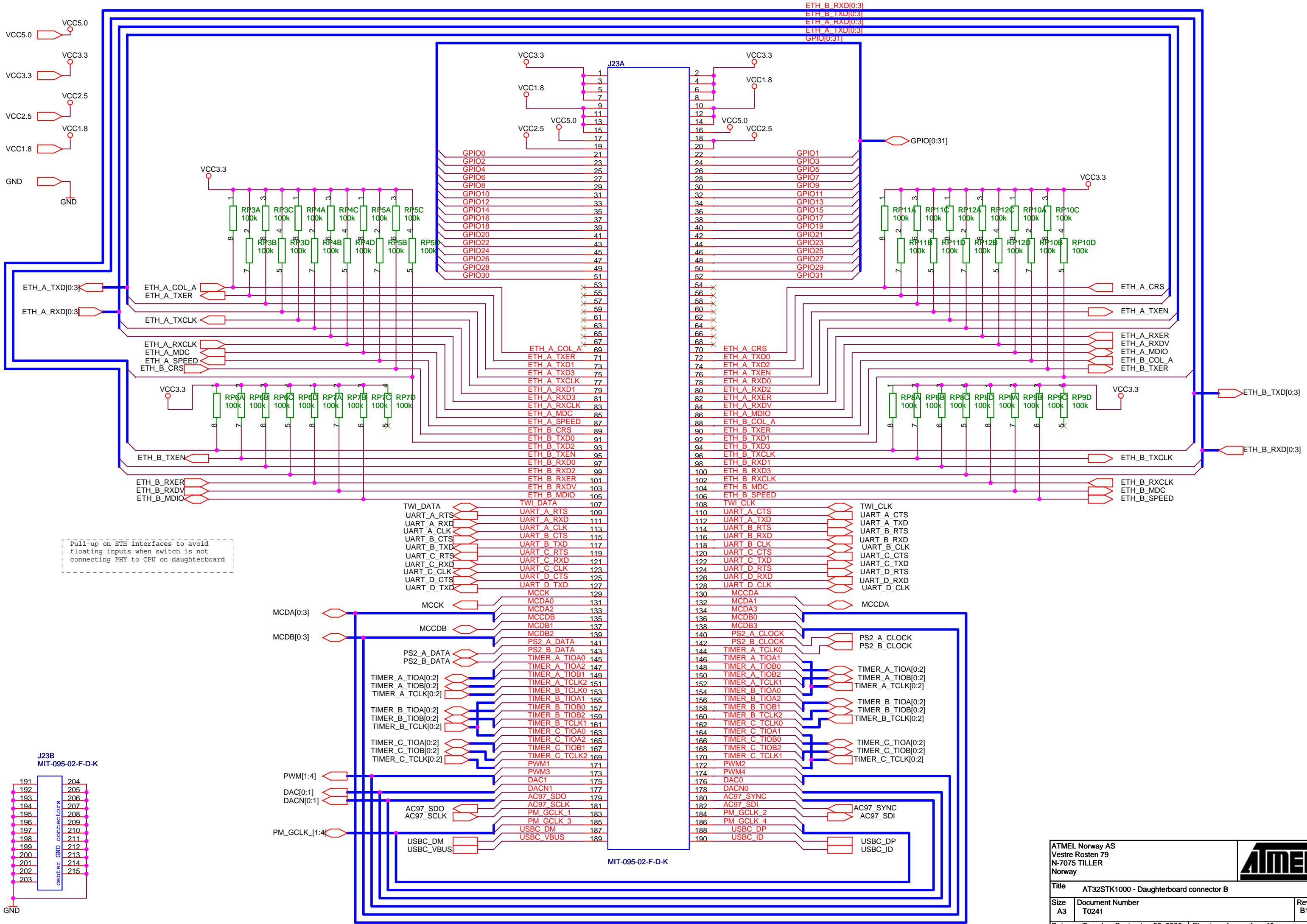


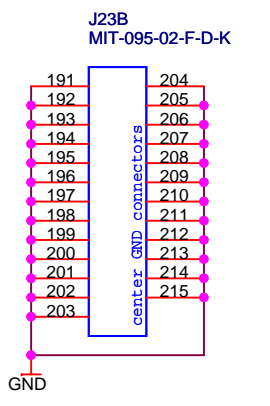
ATMEL Norway AS Vestre Rosten 79 N-7075 TILLER Norway		<b>ATMEL</b>
Title AT32STK1000 - Top level 1		
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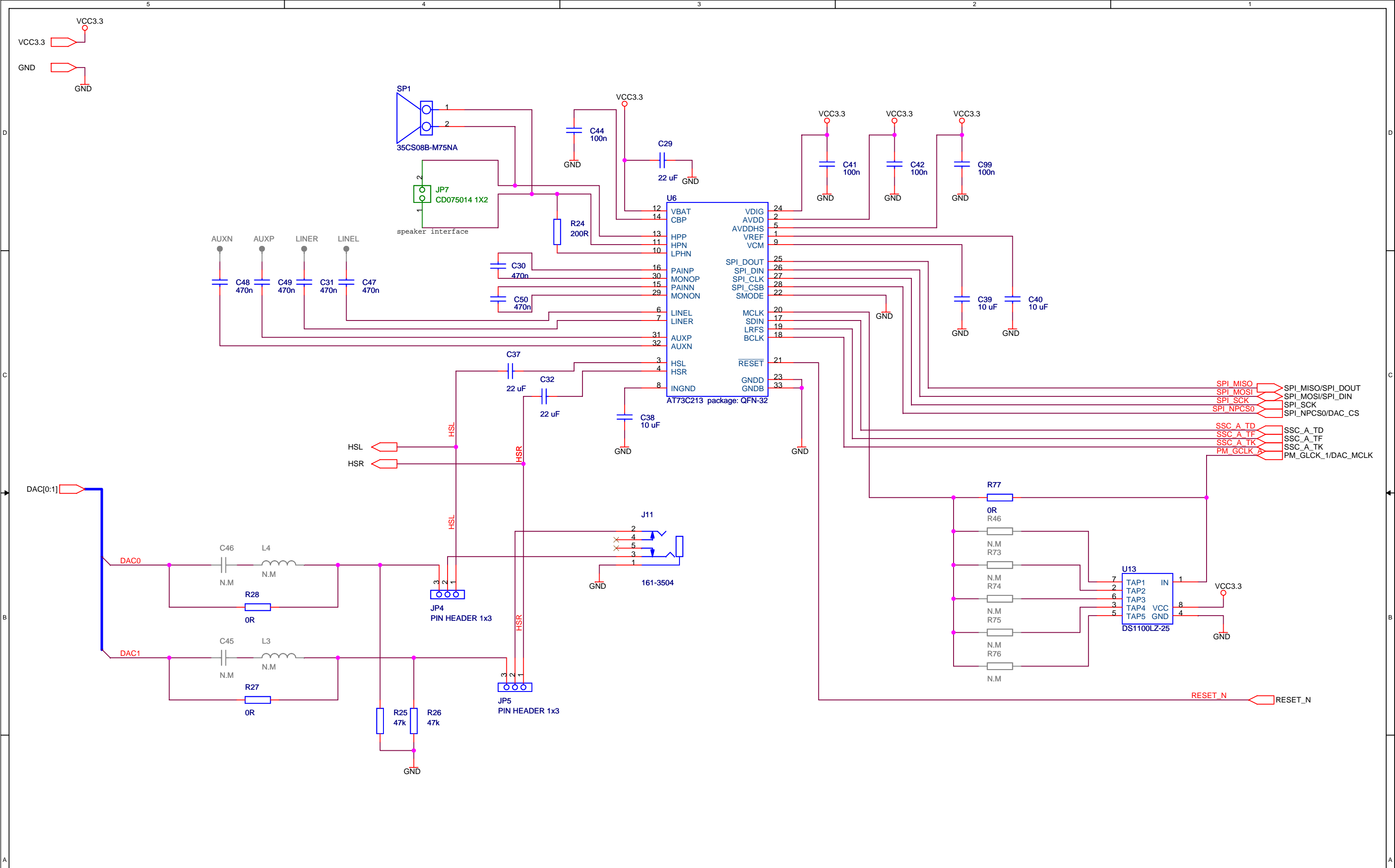


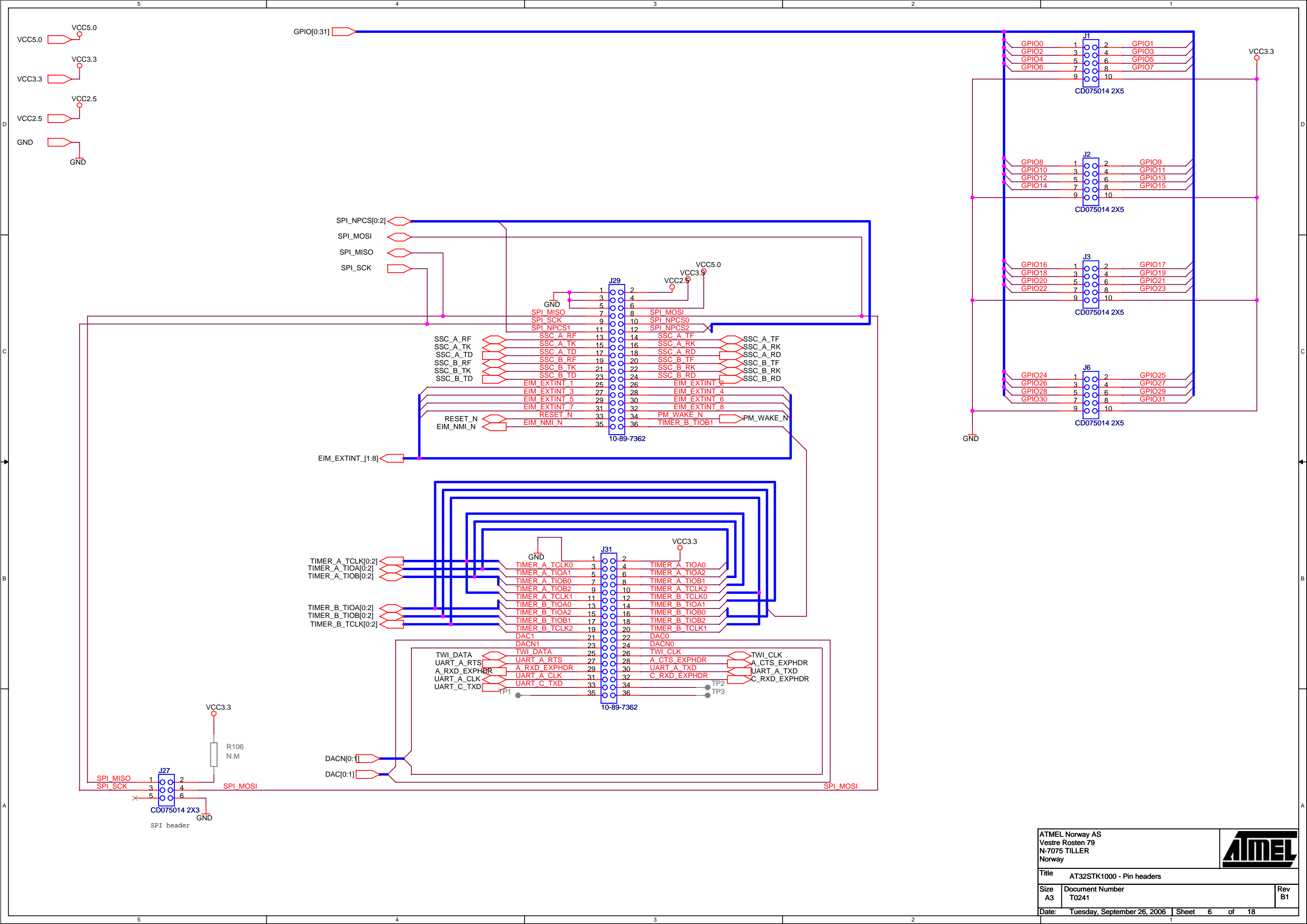




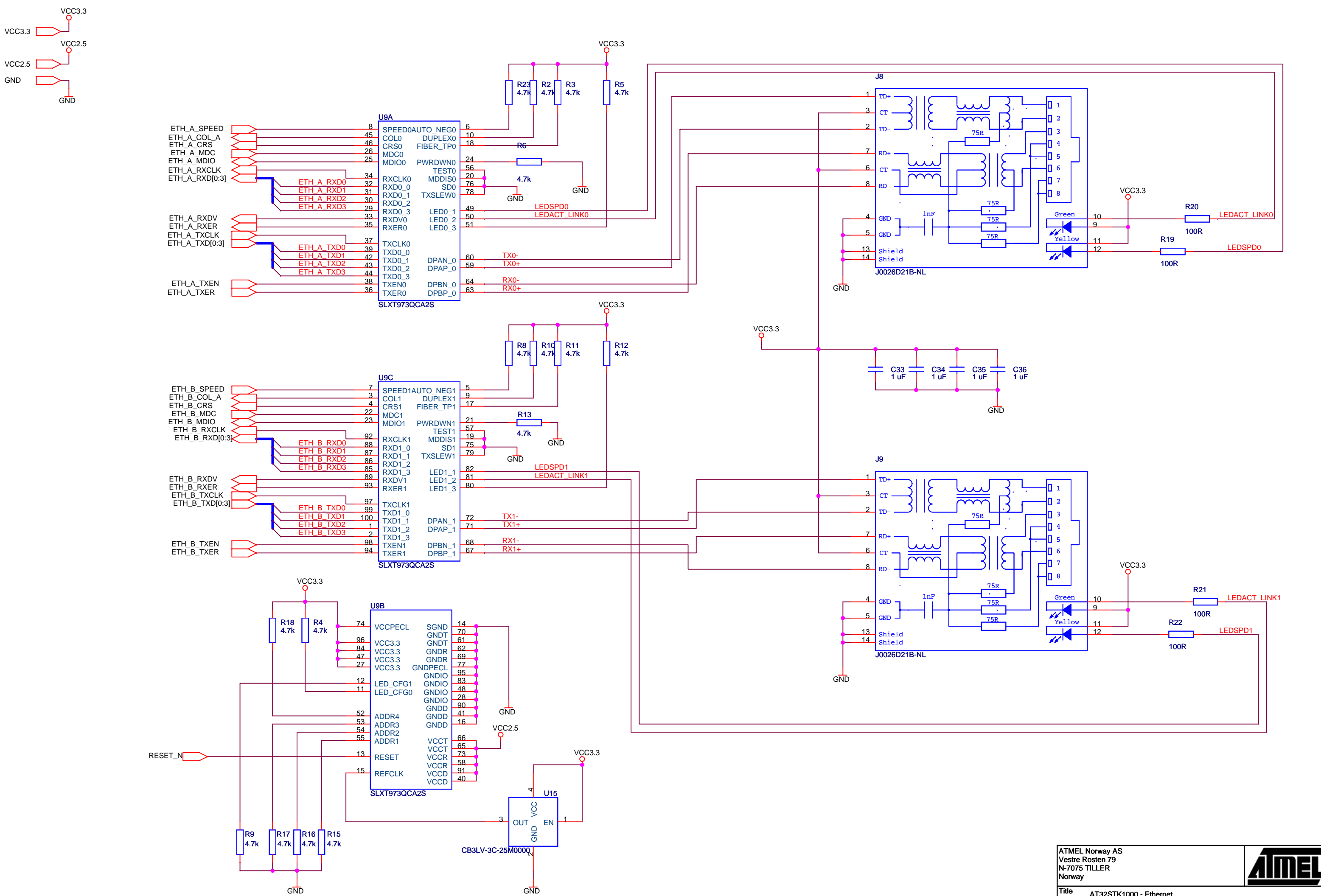
Pull-up on ETH interfaces to avoid floating inputs when switch is not connecting PHY to CPU on daughterboard

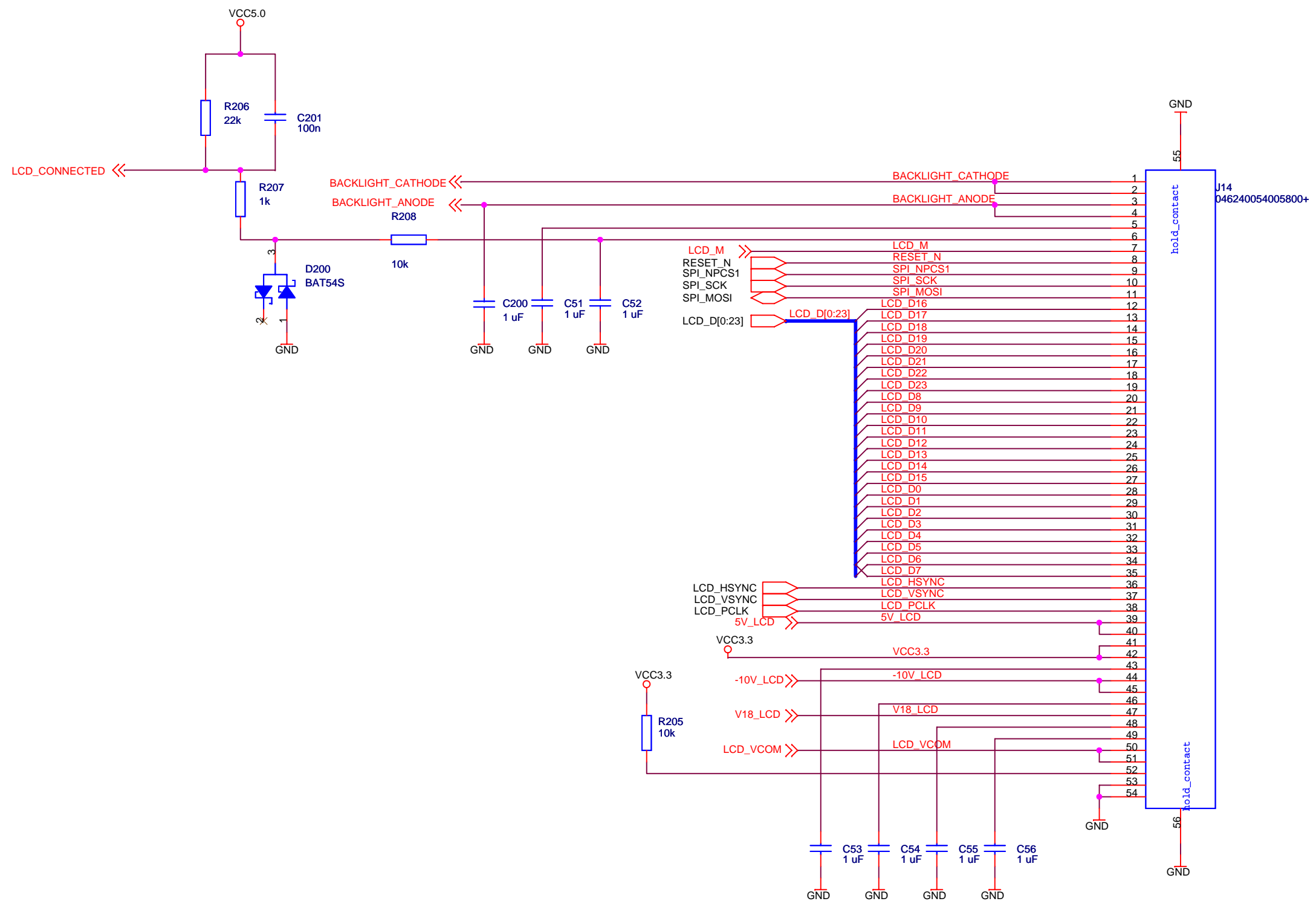
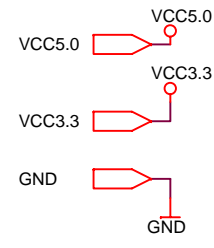




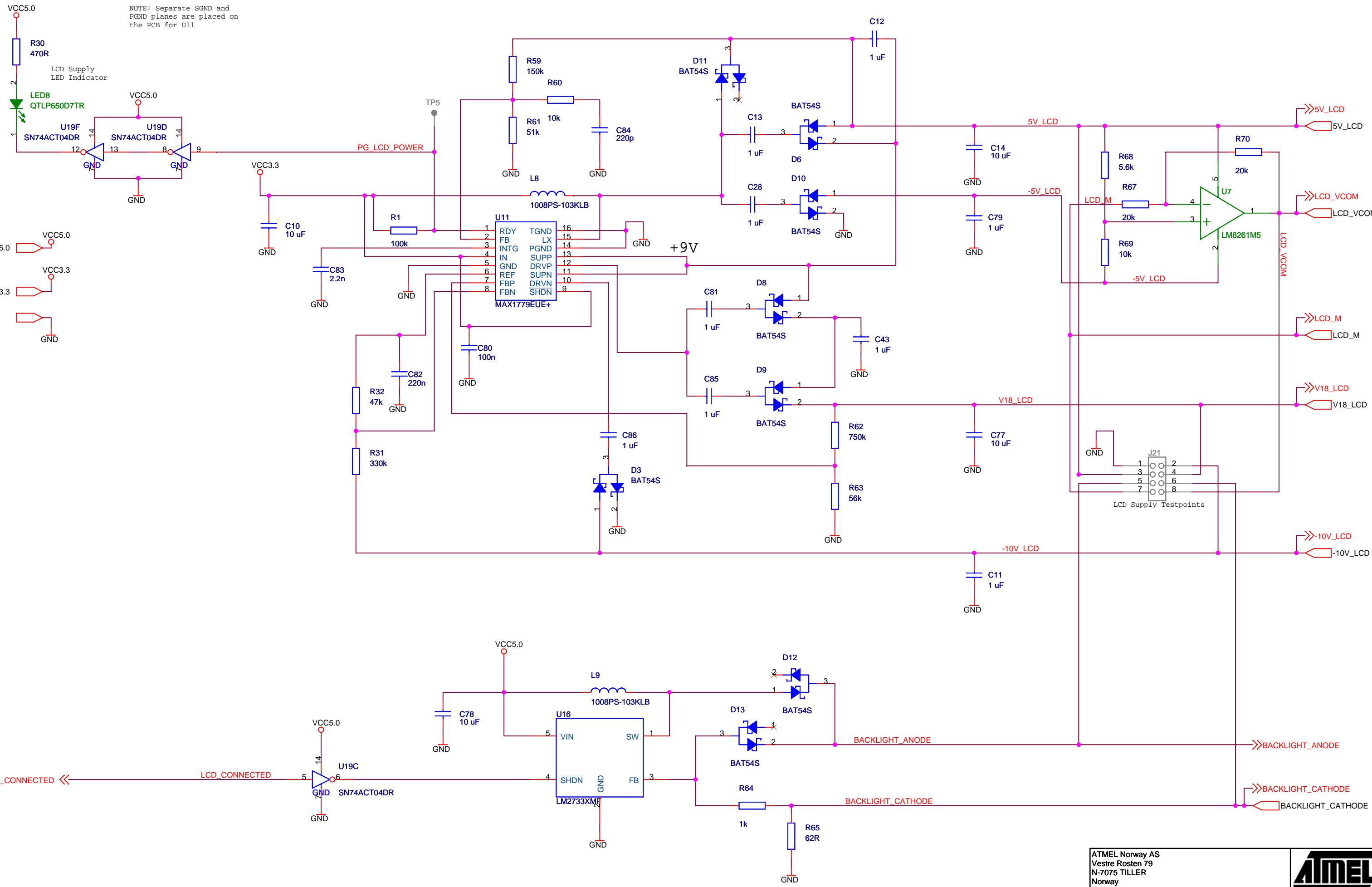


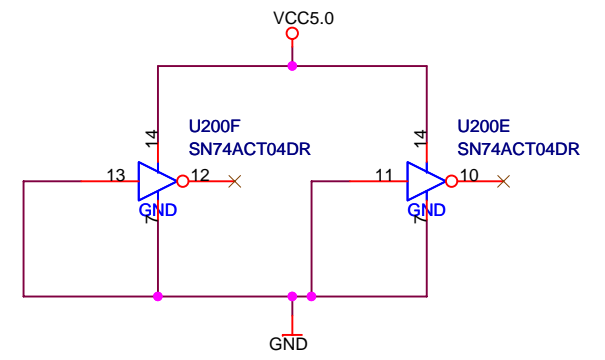
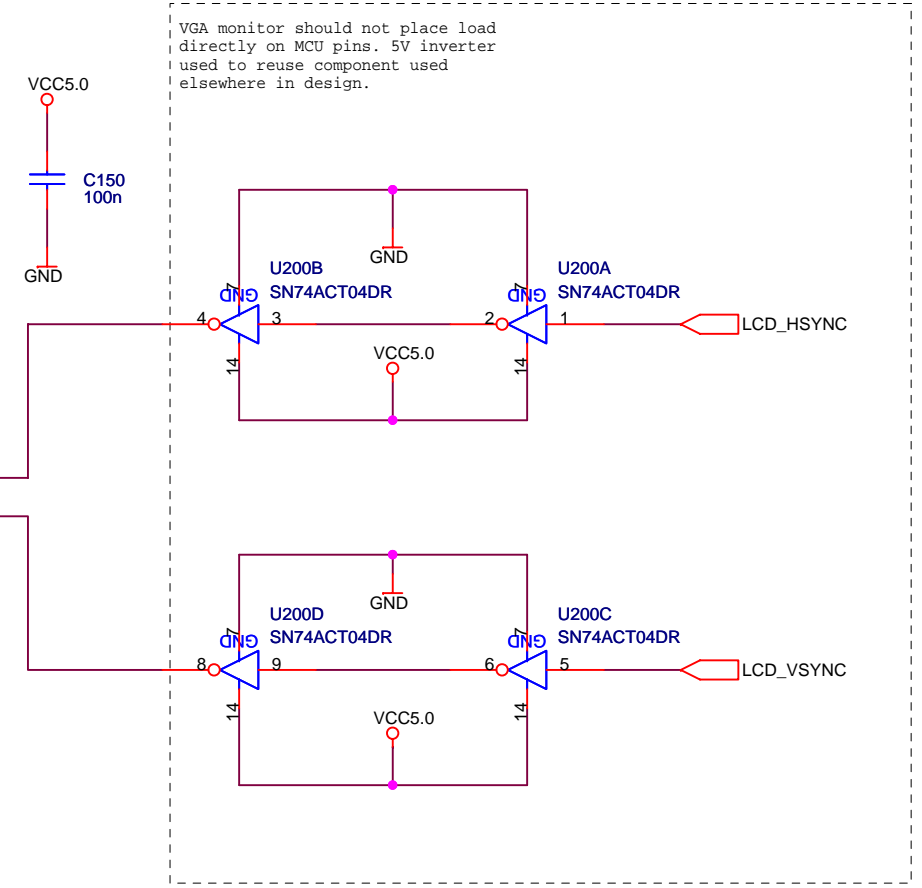
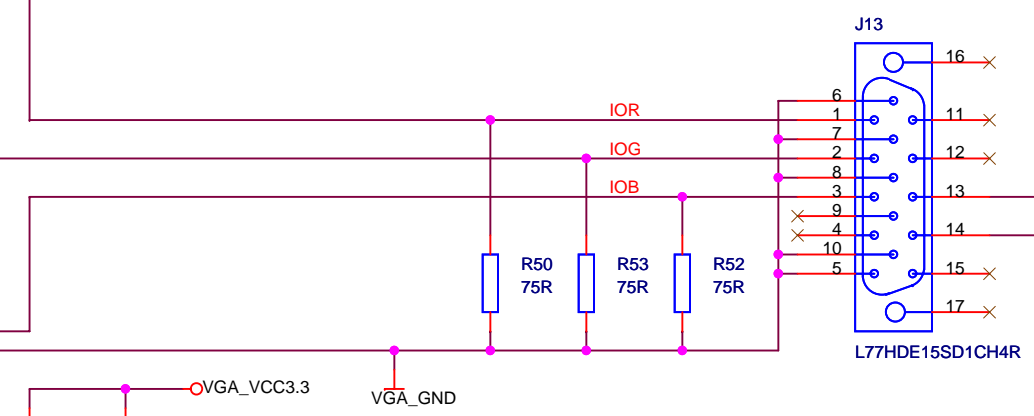
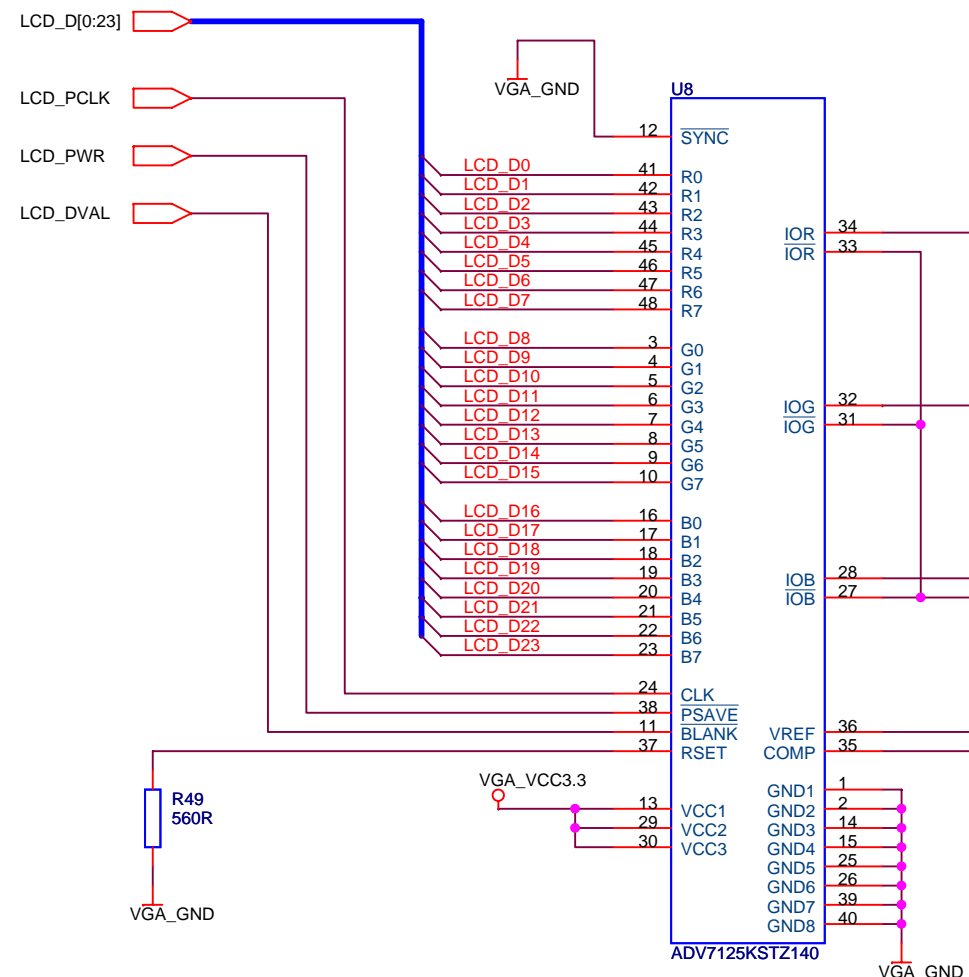
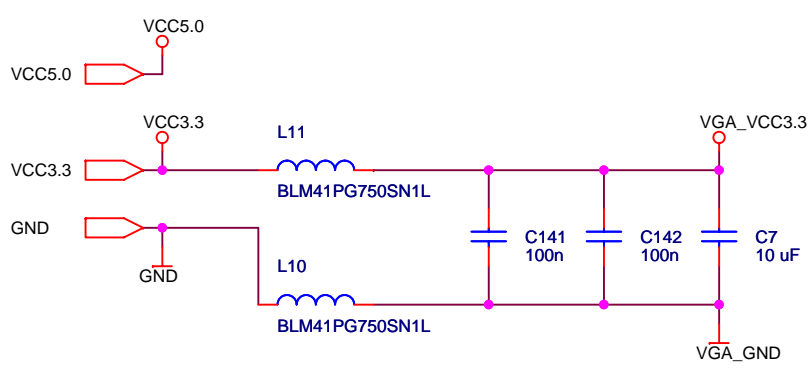


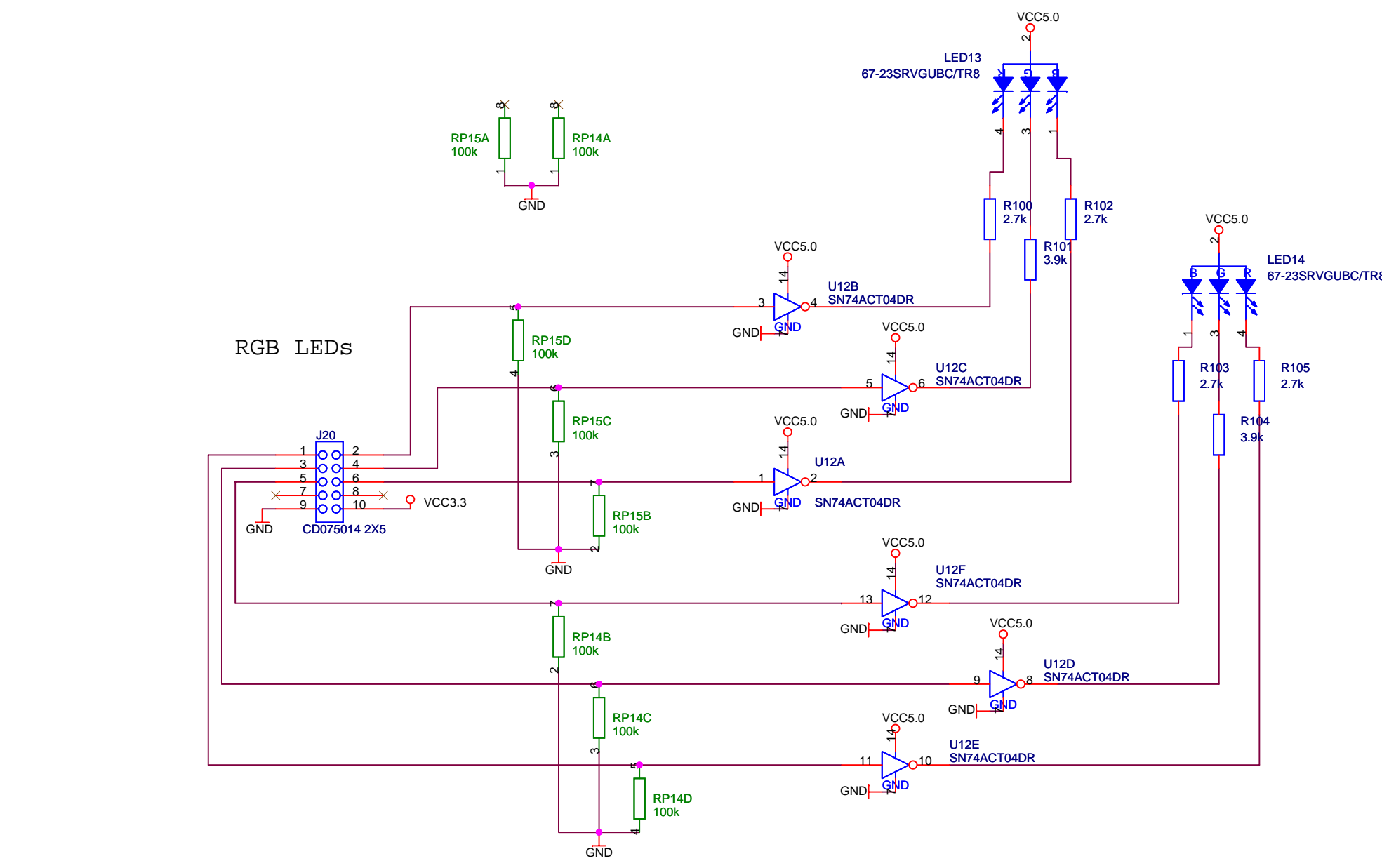
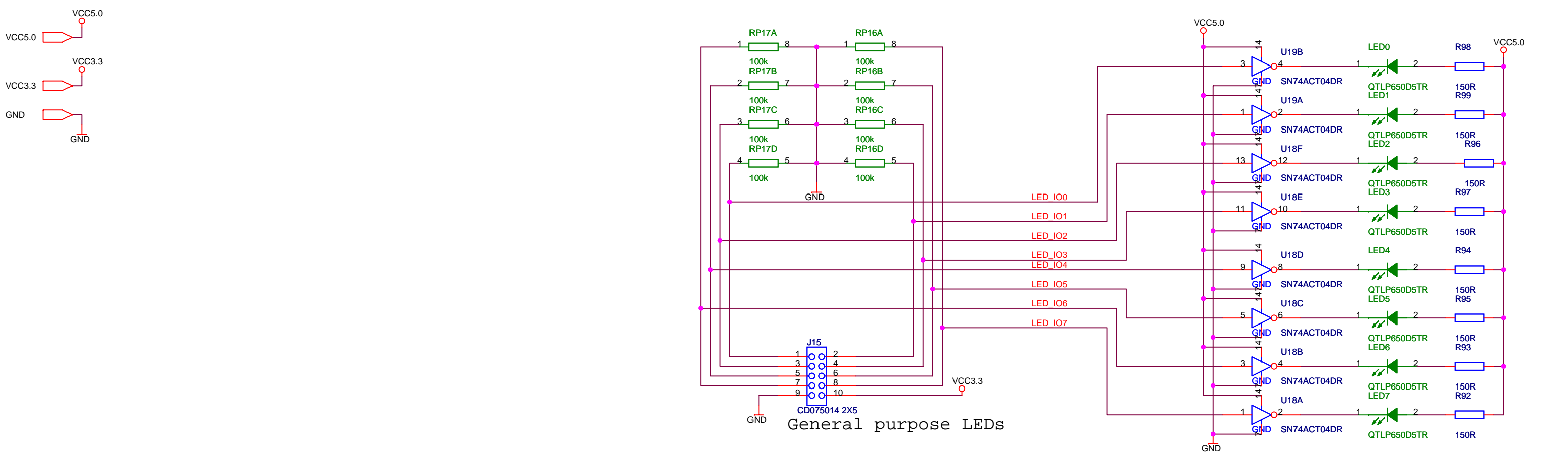


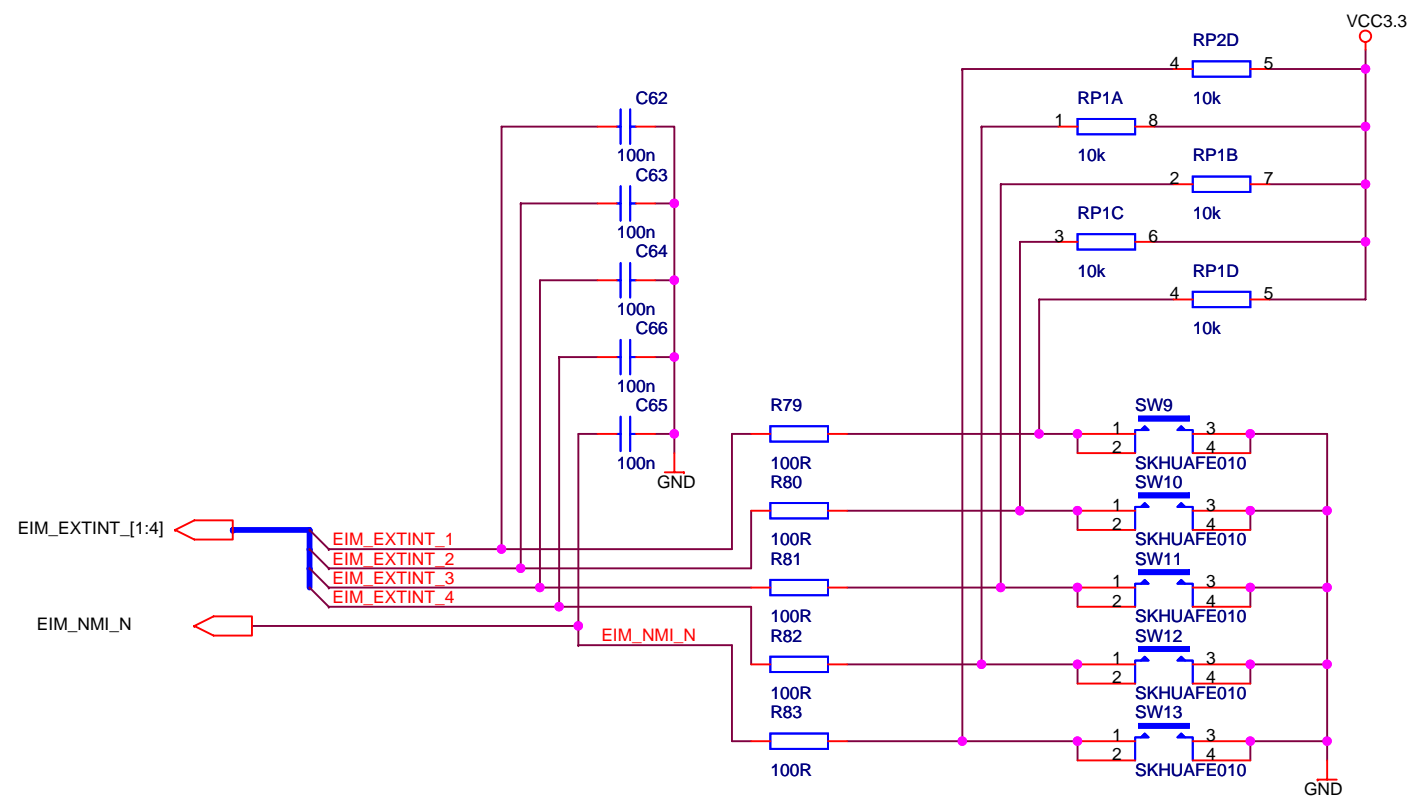




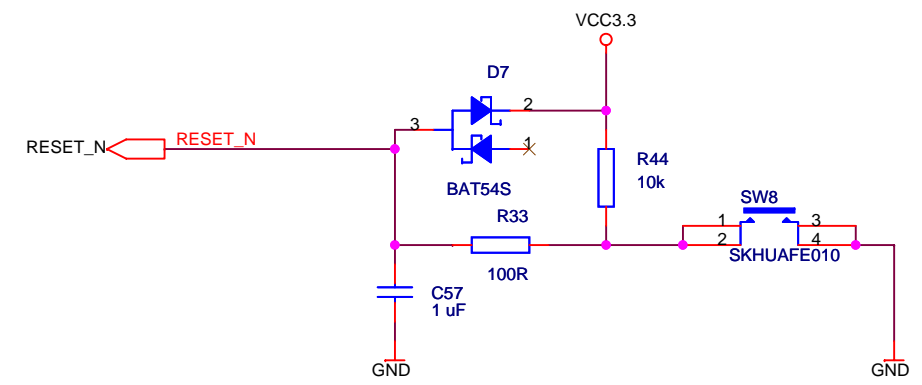




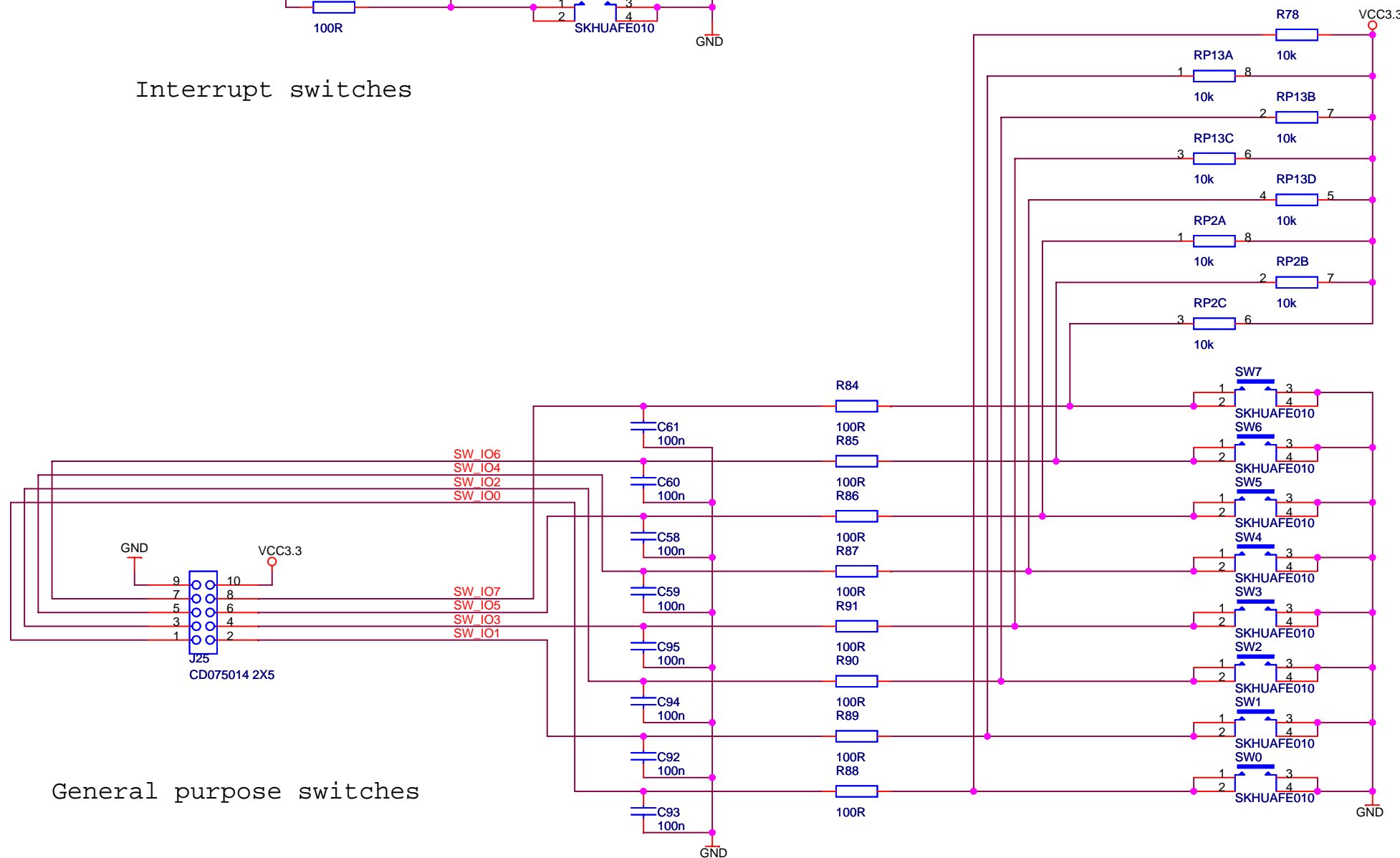




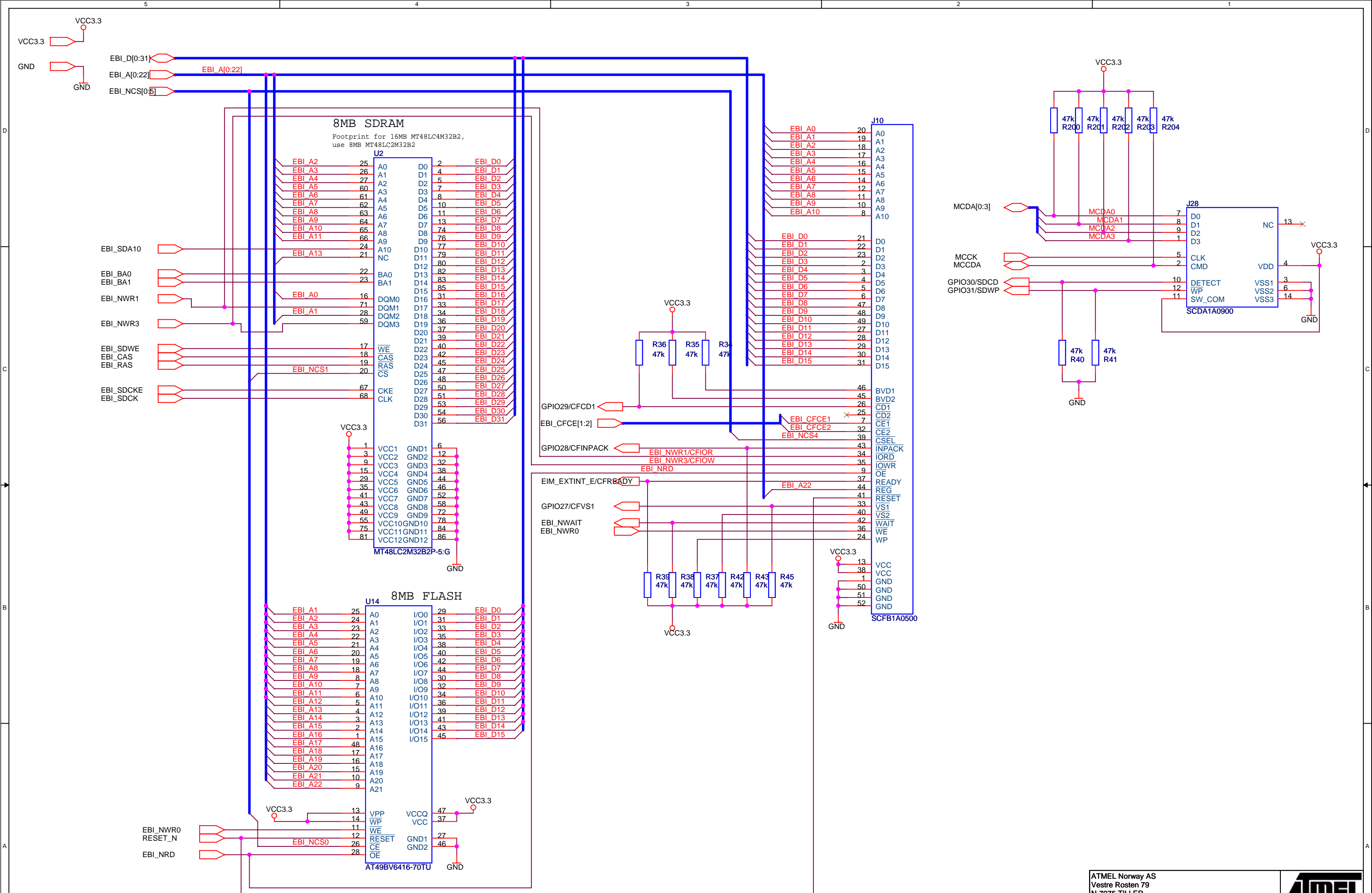
Interrupt switches

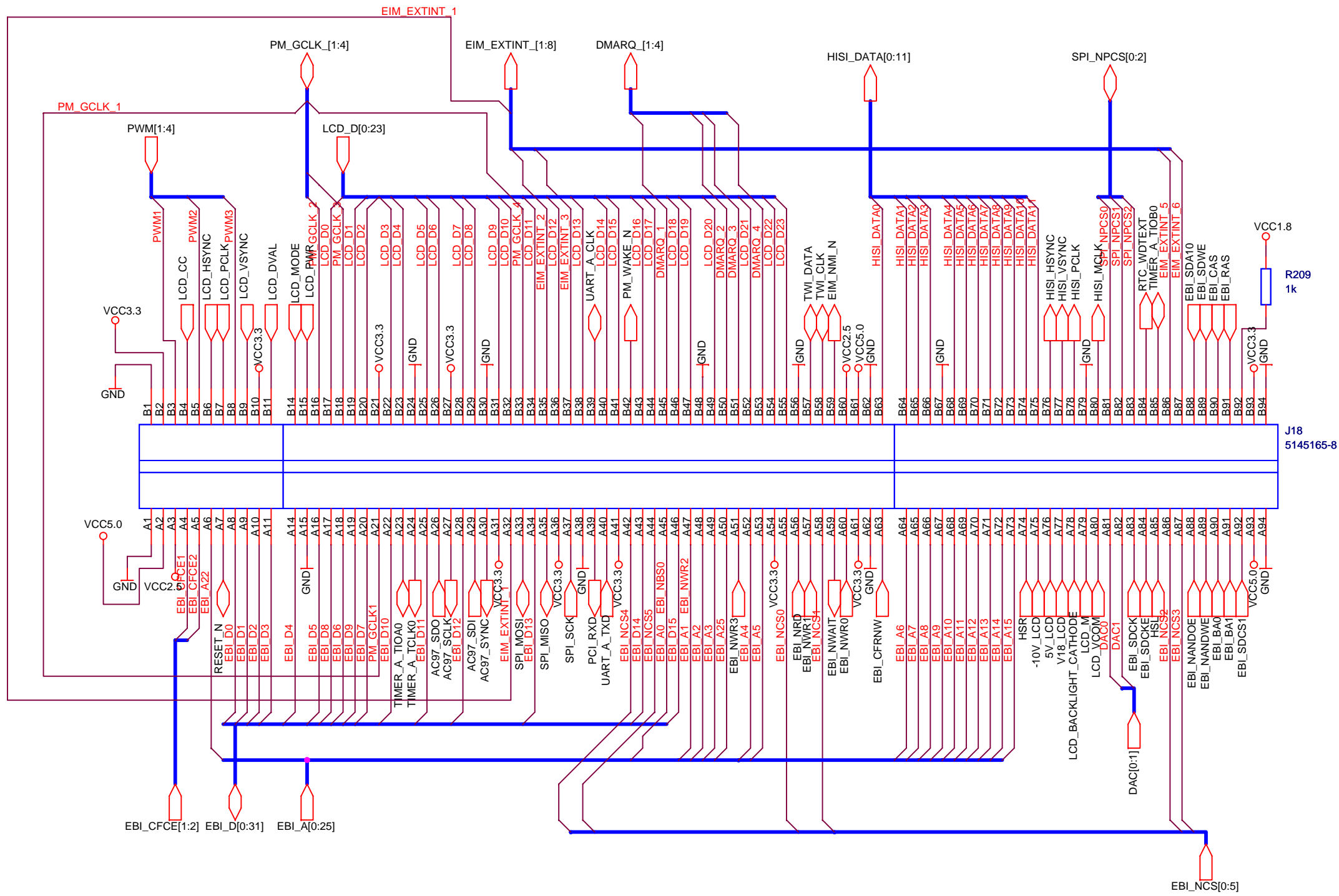
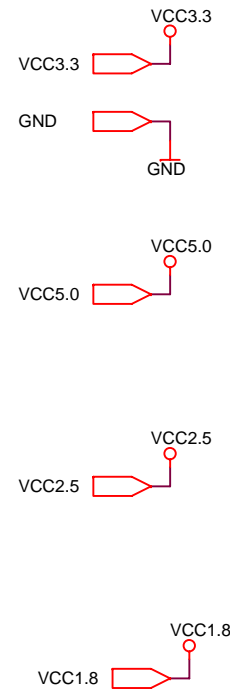


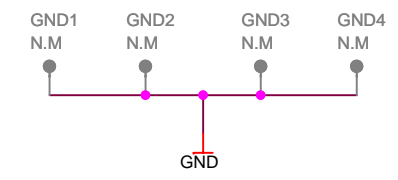
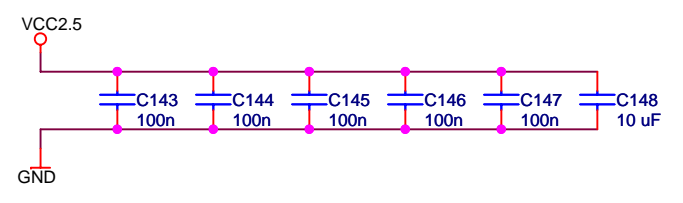
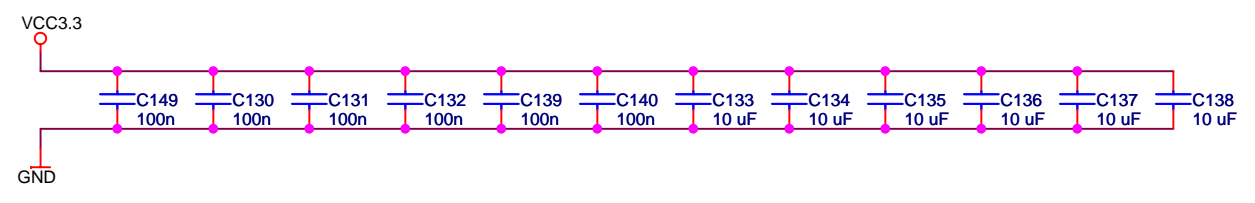
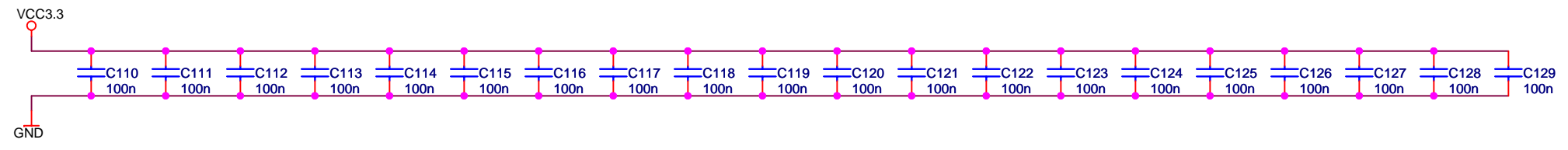
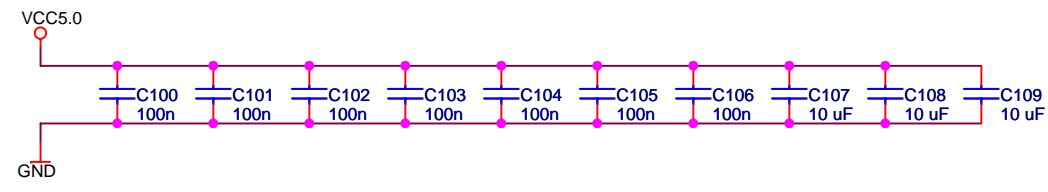
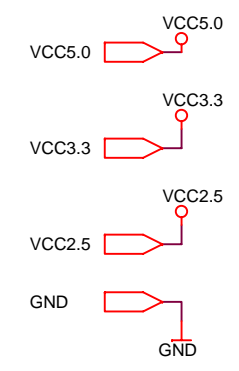
Reset switch



General purpose switches

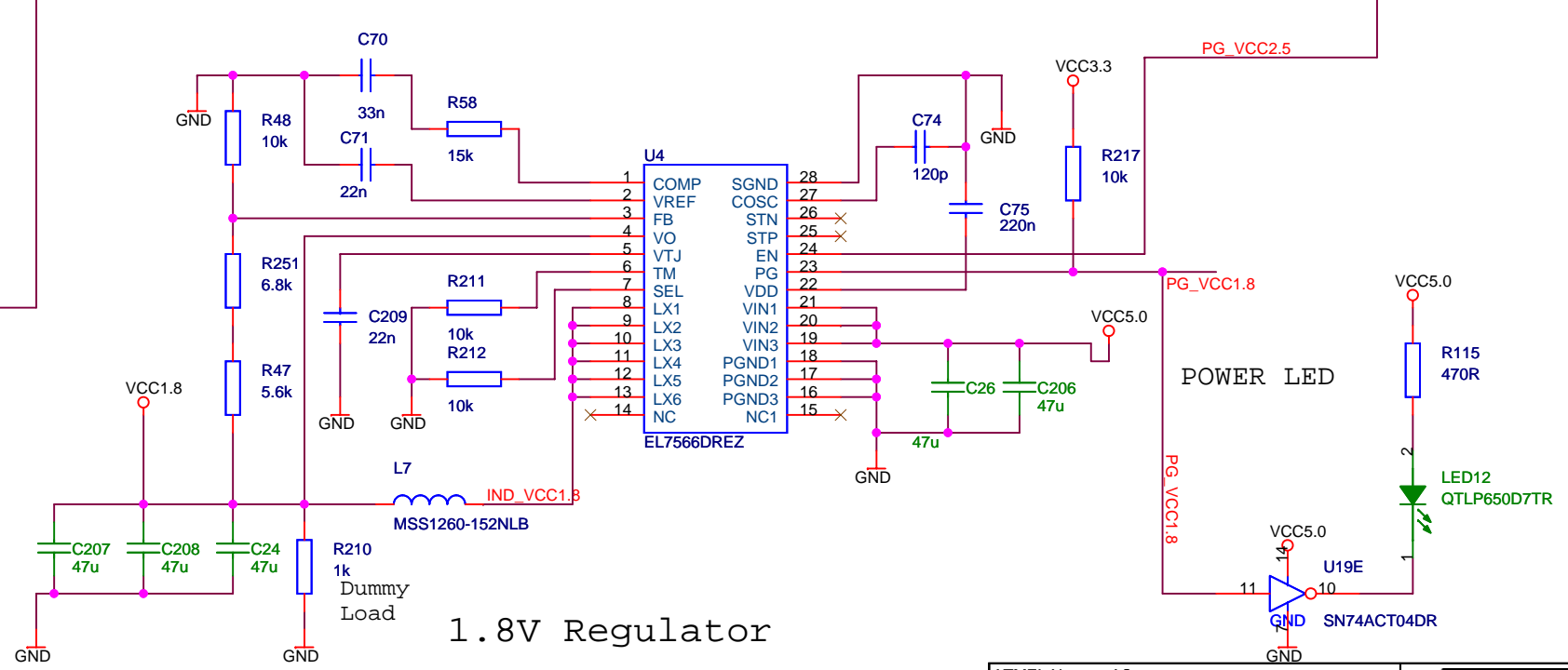
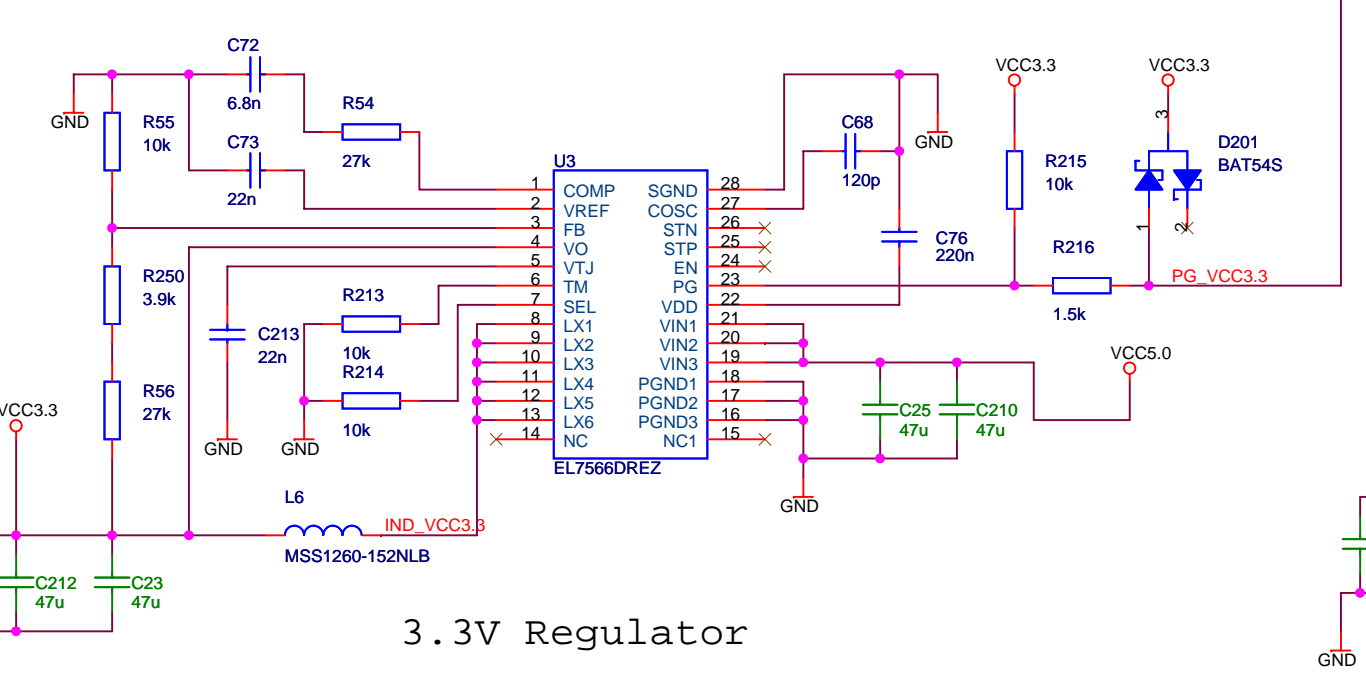
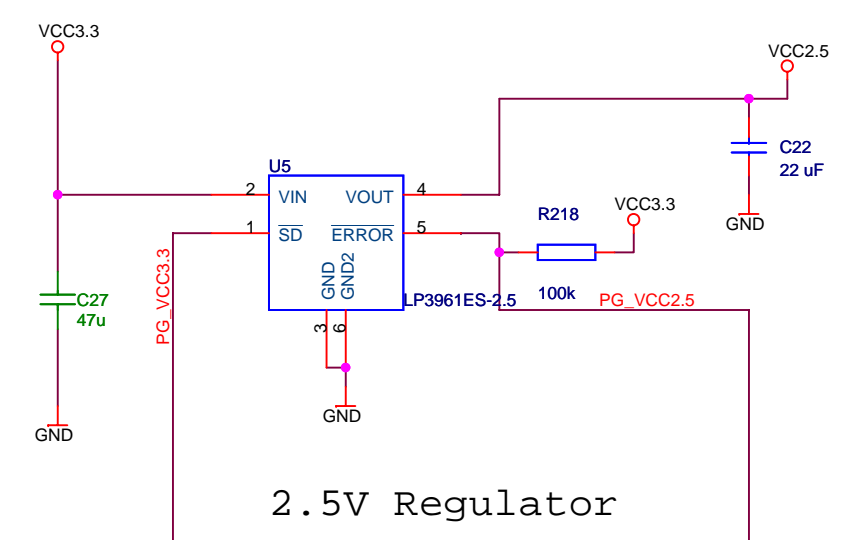
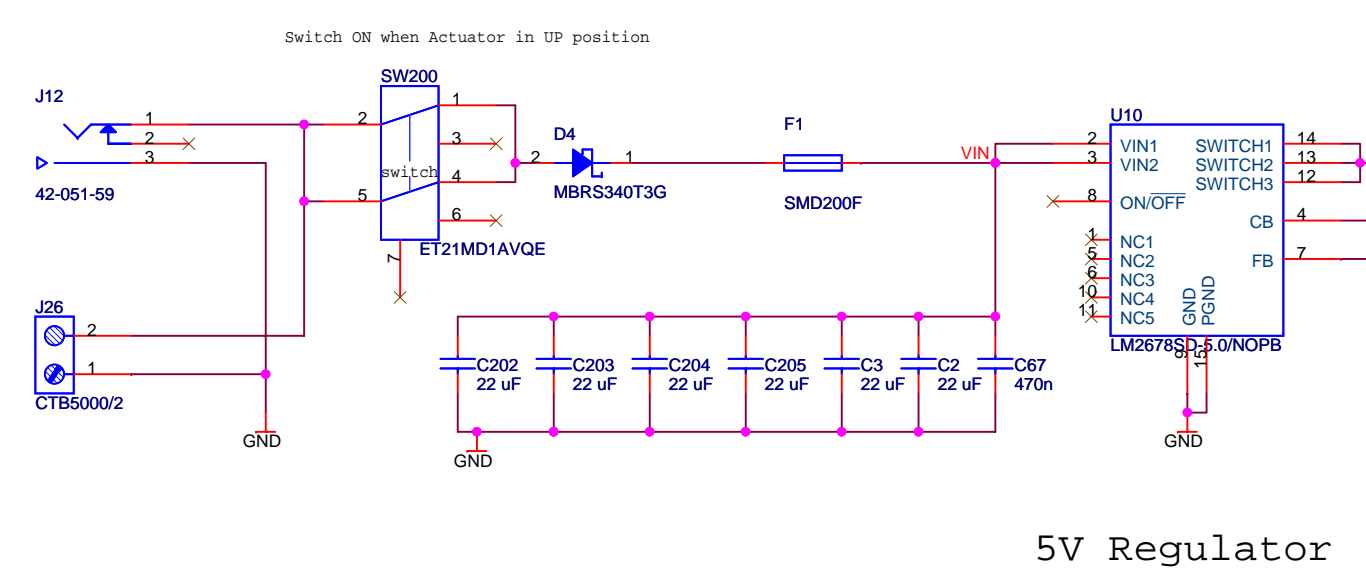
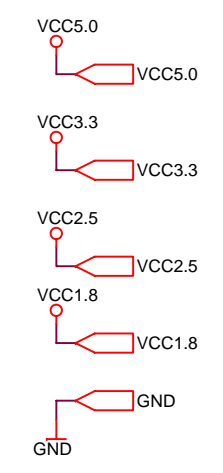
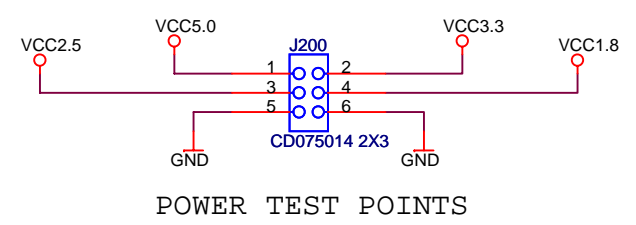






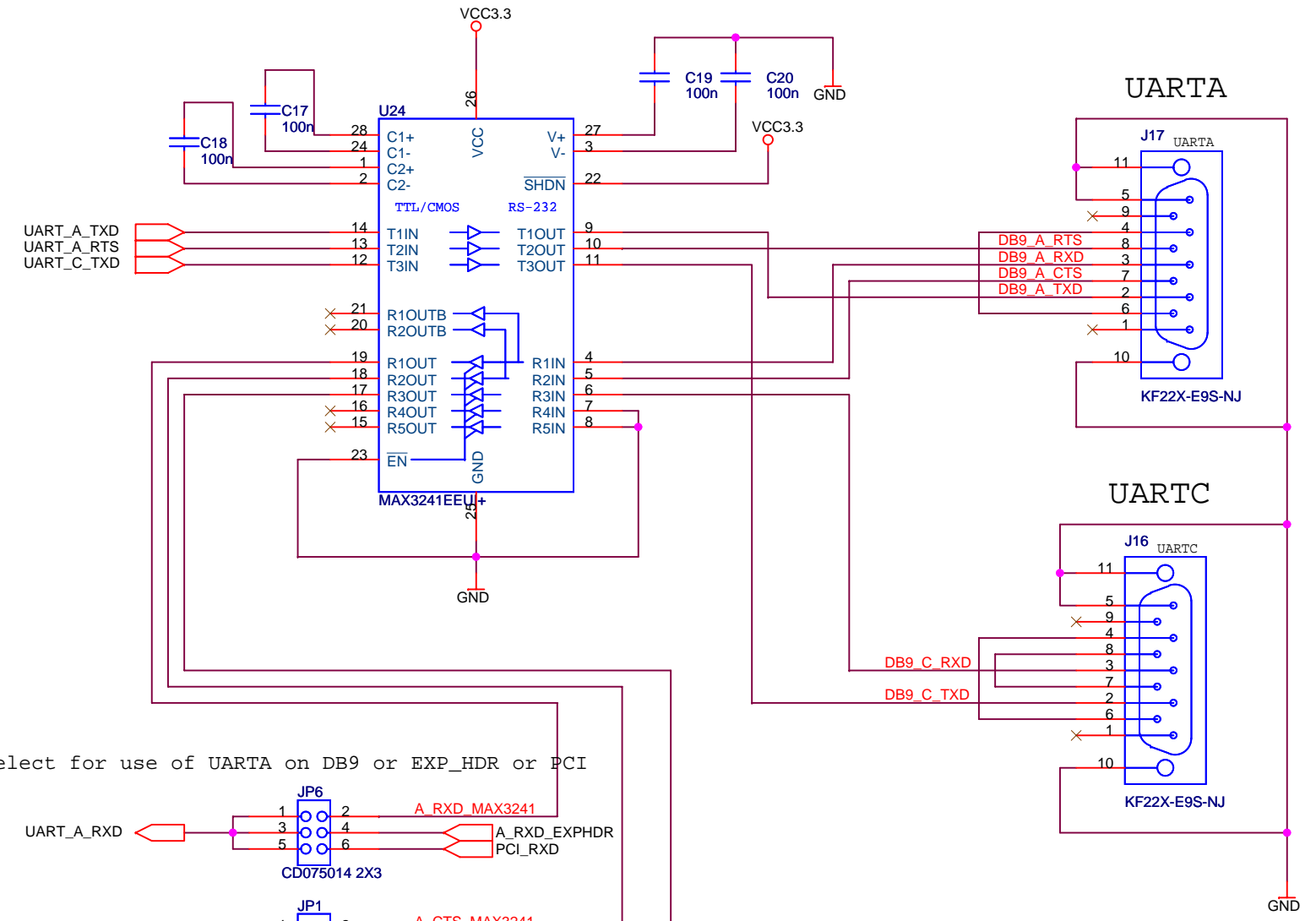
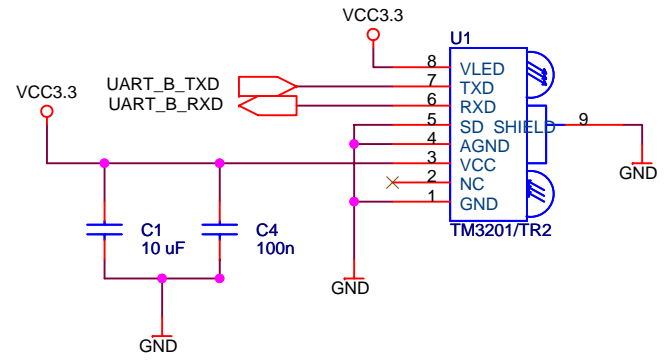
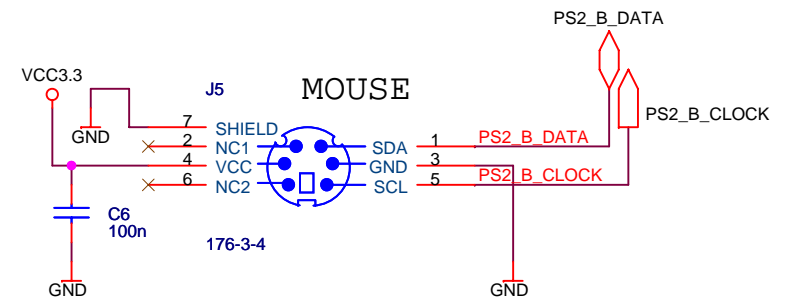
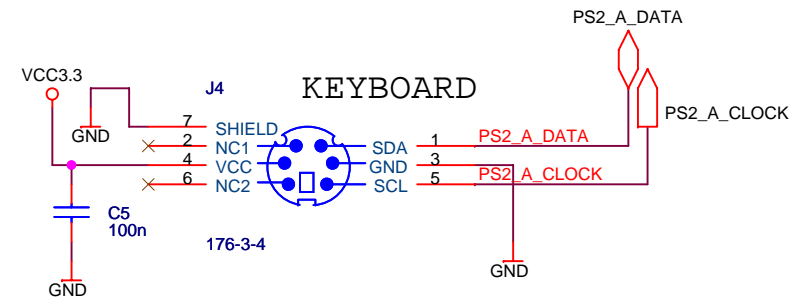
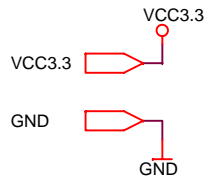


NOTE: Power on sequece is : 5V, followed by 3.3V, followed by 2.5V, followed by 1.8V

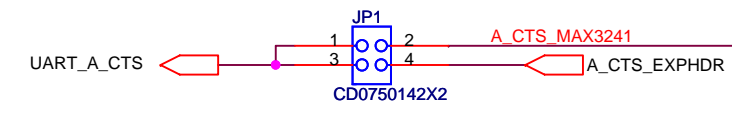
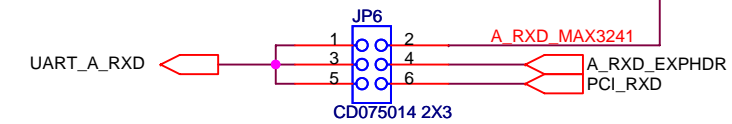


NOTE: Separate SGND and PGND planes are placed on the PCB for U3 and U4

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Jumper Select for use of UARTA on DB9 or EXP\_HDR or PCI



Jumper Select for use of UARTC on DB9 or EXP\_HDR

